SCHEME AND SYLLABI

FOR

THIRD TO EIGHTH SEMESTERS

OF

BACHELOR OF TECHNOLOGY

IN

ELECTRONICS AND COMMUNICATION ENGINEERING

(PART TIME)

FROM 2009 ADMISSION ONWARDS

CALICUT UNIVERSITY (P.O), THENHIPALAM
### Proposed Scheme and Syllabi of B.Tech (Part Time)
Electronics and Communication Engineering (3\(^{rd}\) to 8\(^{th}\) Semesters)

#### 3\(^{rd}\) Semester

<table>
<thead>
<tr>
<th>Code</th>
<th>Subject</th>
<th>Hours/week</th>
<th>Marks</th>
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<td>Signals and Systems</td>
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#### 4\(^{th}\) Semester

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### Elective I

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<td>PTEC09 L02</td>
<td>Numerical methods for Engineers</td>
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## ELECTIVES

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<td>PTEC09 L07</td>
<td>High Speed Digital Design</td>
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<td>PTEC09 L08</td>
<td>Introduction to MEMS</td>
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<td>PTEC09 L09</td>
<td>Multimedia Communication Systems</td>
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<td>Cryptography &amp; Network security</td>
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<td>PTEC09 L12</td>
<td>Antenna Theory &amp; Design</td>
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<td>Microwave Active Devices &amp; Circuits</td>
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<td>Advanced semiconductor device technology</td>
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<td>Mobile computing</td>
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<td>Image &amp; video Processing</td>
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<td>Advanced digital signal Processing</td>
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<td>PTEC09 L23</td>
<td>Data Structures &amp; Algorithms</td>
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## GLOBAL ELECTIVES

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Objective

Objective of this course is to inculcate the students an adequate understanding of the basic concepts of probability theory to make them develop an interest in the area which may find useful to pursue their studies. Also it is intended to stimulate the students understanding of the Z-transform. A study of some important partial differential equations is also included to make the student get acquainted with the basics of PDE.

Module I: Probability Distributions

Module II: Z Transforms

Module III: Series Solutions of Differential Equations

Module IV: Partial Differential Equations
Introduction – Solutions of equations of the form \(F(p,q) = 0\); \(F(x,p,q) = 0\); \(F(y,p,q) = 0\); \(F(z,p,q) = 0\); \(F_1(x,q) = F_2(y,q)\); Clairaut’s form, \(z = px + qv + F(p,q)\); Lagrange’s form, \(Pp + Qq = R\) – Classification of Linear PDE’s – Derivation of one dimensional wave equation and one dimensional heat equation – Solution of these equation by the method of separation of variables – D’Alembert’s solution of one dimensional wave equation.
## Text Books

### Module I:
Richard A Johnson, CB Gupta, *Miller and Freund’s Probability and statistics for Engineers, 7e*, Pearson Education - Sections: 4.1, 4.2, 4.3, 4.4, 4.6, 4.8, 5.1, 5.2, 5.5, 5.7

### Module II:
P Ramesh Babu, R Ananda Natarajan, *Signals and Systems, 2e*, Scitech Publications.  Sections: 10.1, 10.2, 10.3, 10.4, 10.5.1, 10.5.2, 10.5.3, 10.5.4, 10.5.5, 10.5.6, 10.5.7, 10.5.8, 10.5.12, 10.5.13, 10.6, 10.10

### Module III:

### Module IV:

### Reference books
University Examination Pattern

**PART A:** Short answer questions (one/two sentences)  
5 x 2 marks = 10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions  
4 x 5 marks = 20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions  
4 x 10 marks = 40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70

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**Internal Continuous Assessment (Maximum Marks: 30)**

- 60% - Tests (minimum 2)
- 30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- 10% - Regularity in the class
PTEC09 302: NETWORK ANALYSIS & SYNTHESIS

Teaching scheme
3 hours lecture and 1 hour tutorial per week

Credits: 5

Objectives
- To expose the students to the basic concepts of electric circuits and their analysis in time and frequency domain
- To introduce the concept of filter circuits and design of passive filters
- To introduce the techniques of network Synthesis

Module I
Signal representations: Impulse, step, pulse, ramp and exponential functions

Module II
Network functions- The concept of complex frequency –driving point and transfer functions-Impulse response-Poles and Zeros of network functions, their locations and effects on the time and frequency domain responses. Restriction of poles and zeros in the driving point and transfer function. Time domain behaviour from the pole—zero plot. Frequency response plots-Bode plot Parameters of two-port network – impedance, admittance, transmission and hybrid - Conversion formulae. Analysis of interconnected two port networks-parallel, series, and cascade connections of 2 port networks - Characteristic impedance and propagation constant Attenuators -propagation constant, types of attenuators-T and Bridged T - compensated attenuators.

Module III
Filters- Introduction and basic terminology –types of filtering-L.P filter basics-Butterworth LP filter transfer characteristics- Basic passive realization of Butterworth transfer functions. Frequency transformations- Transformation to high pass, band pass and band elimination. Chebyshev filters – Characteristics-poles of the Chebyshev function

Module IV
Synthesis: positive real functions - driving point functions - Brune's positive real functions - properties of positive real functions - testing driving point functions - application of maximum module theorems - properties of Hurwitz polynomials - even and odd functions - Strum's theorem - driving point synthesis - RC elementary synthesis operations - LC network synthesis - properties of RC network functions - foster and Cauer forms of RC and RL networks
### Text Books
1. Van Valkenberg, *Network Analysis*, Prentice Hall of India

### Reference Books
1. Choudary D R, *Networks and Systems*, New Age International

### Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

### End-Term Examination

**PART A:** Short answer questions (one/two sentences) 5 x 2 marks = 10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions 4 x 5 marks = 20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions 4 x 10 marks = 40 marks
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTEC09 303: SIGNALS AND SYSTEMS

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives
- To give basic ideas about different signals and systems
- To impart basic knowledge about the representations and transforms of the signals

Module I
Introduction to signals and systems-classification of signals-basic operations on signals-elementary signals-concept of system-properties of systems-stability, invertibility, time invariance, linearity, causality, memory, time domain description, convolution-impulse response-representation of LTI systems-differential equation and difference equation representation of LTI systems.

Module II
Fourier representation of continuous time signals-Fourier transform-existence of the Fourier integral-FT theorems-energy spectral density and power spectral density-frequency response of LTI systems-correlation theory of deterministic signals-condition for distortionless transmission through an LTI system-transmission of a rectangular pulse through an ideal low pass filter-Hilbert transform-sampling and reconstruction.

Module III
Fourier representation of discrete time signals-discrete Fourier series and discrete Fourier transform-Laplace transform analysis of systems-relation between transfer function and differential equation-causality and stability-inverse system-determining the frequency response from poles and zeros.

Module IV
Z-transform-definition-properties of the region of convergence-properties of the Z-transform-analysis of LTI systems-relating transfer function and difference equation-causality-inverse systems-determining the frequency response from poles and zeros.

Text Books
1. S. Haykin and B. V. Bean, Signals and Systems, John Wiley & Sons, NY

Reference Books
5. S. Palani, Signals, Systems Ane Book Pvt. Ltd., New Delhi, 2009
### Internal Continuous Assessment *(Maximum Marks-30)*

- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity in the class

### End-Term Examination

**PART A:** *Short answer questions (one/two sentences)*  
5 x 2 marks = 10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** *Analytical/Problem solving questions*  
4 x 5 marks = 20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** *Descriptive/Analytical/Problem solving questions*  
4 x 10 marks = 40 marks  
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
OBJECTIVE

- THIS PAPER EXPOSES THE STUDENTS TO DIGITAL FUNDAMENTALS.
- AFTER STUDYING THIS SUBJECT THE STUDENT WILL BE ABLE TO DESIGN, ANALYZE AND INTERPRET COMBINATIONAL AND SEQUENTIAL DIGITAL CIRCUITS OF MEDIUM COMPLEXITY.

Module I

Boolean algebra: Theorems and operations- Boolean expressions and truth tables- Multiplying out and factoring expressions- Exclusive-OR and equivalence operations.

Combinational logic design using truth table- Minterm and Maxterm expansions- Incompletely specified functions.


Module II


Synthesis of combinational logic functions using MSIs - multiplexers- demultiplexers- decoders- encoders

Introduction to TTL and ECL logic families: Basic working of a TTL NAND gate- characteristics of a TTL NAND gate- important specifications – Basic working of ECL gate- Transfer characteristics of a ECL NAND gate- important specifications

Module III


Module IV  
Synchronous sequential circuits: Finite State Machines- Mealy & Moore types- Basic design steps- Design of counters, sequence generators, and sequence detectors - Design of simple synchronous machines – state minimization- ASM charts

**Text books**

**Reference**
4. Taub and Schilling, *Digital principles and applications*, TMH  
5. Volnei A Pedroni, Digital electronics and design with VHDL, Elsevier  

**Internal Continuous Assessment (Maximum Marks-30)**
60% - Tests (minimum 2)  
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.  
10% - Regularity in the class

**University Examination Pattern**

**PART A:** Short answer questions (one/two sentences)  
\[ 5 \times 2 \text{ marks} = 10 \text{ marks} \]
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions  
\[ 4 \times 5 \text{ marks} = 20 \text{ marks} \]
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions  
\[ 4 \times 10 \text{ marks} = 40 \text{ marks} \]
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
Objectives

- To Study the, operation, performance and characteristics of different dc and ac machines
- To familiarise various electrical measuring instruments

Module I

Module II
Principle of indicating instruments – moving coil, moving iron and dynamometer type instruments – extension of range of ammeter and voltmeter using current transformer and voltage transformer – principle and working of induction type energy meter

Module III

Module IV

Text Books
1. Vincent Del Toro, Electrical Engineering Fundamentals, Prentice-Hall of India
2. Hughes, Electrical technology, Tata Mc Graw Hill

Reference Books
2. P.S. Bhimbra, Electrical Machinery, Khanna Publishers
3. K. Murukesh Kumar, DC machines and Transformers, Vikas Publishing house Pvt Ltd
**Internal Continuous Assessment** *(Maximum Marks-30)*

- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity in the class

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**University Examination Pattern**

**PART A:** *Short answer questions (one/two sentences)*  
5 x 2 marks = 10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** *Analytical/Problem solving questions*  
4 x 5 marks = 20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** *Descriptive/Analytical/Problem solving questions*  
4 x 10 marks = 40 marks  
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTEC09 306(P) DIGITAL ELECTRONICS LAB

Teaching Scheme
2 hours practical per week

Objectives

- To provide experience on design, testing, and analysis of digital electronic circuits

1. Realization of logic gates using diodes and transistors.
2. Characteristics of TTL Gates
3. Realization of logic gates using universal gates
5. Seven segment display
6. Realization of Mux, Deconder and Encoder using basic gates
7. Combinational logic design using Decoders and Muxs
8. Half and Full adders and Subtractors.
9. 4 bit adder-subtractor IC & BCD adder circuit
10. Flip-Flop Circuit (RS Latch, JK, T, D and Master Slave) using basic gates.
11. Asynchronous Counters
12. Johnson and Ring Counters.
13. Synchronous counters.
15. A sequence detector Circuit.
16. Registers.

Note: A minimum of 10 experiments must be conducted

<table>
<thead>
<tr>
<th>Internal Continuous Assessment (Maximum Marks-50)</th>
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<tbody>
<tr>
<td>60% - Laboratory practical and record</td>
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<tr>
<td>30% - Test/s</td>
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<tr>
<td>10% - Regularity in the class</td>
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<table>
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<tr>
<th>Semester-End Examination (Maximum Marks-50)</th>
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</thead>
<tbody>
<tr>
<td>70% - Procedure, conducting experiment, results, tabulation, and inference</td>
</tr>
<tr>
<td>20% - Viva voce</td>
</tr>
<tr>
<td>10% - Fair record</td>
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PTEC09 307 (P) Electrical Engineering Lab

Teaching Scheme
2 hours practical per week

Credits: 2

Objectives
- To Familiarise various electrical measurement equipments and measurement methods
- To obtain the performance characteristics of dc and ac machines

1. Calibration of single phase energy meter by direct loading
2. Load test on DC shunt generator
   a. Plot external characteristics
   b. Deduce internal characteristics
3. Load test on 3-phase squirrel cage induction motor.
4. Load test on DC series motor
   a. Plot the performance characteristics
6. Determination of V-I characteristics of linear resistance and incandescent lamp
7. No-load and blocked rotor tests on slip ring induction motor
   a. Determine equivalent circuit parameters
   b. Predetermine the torque, line current and efficiency from equivalent circuit corresponding to a specified slip.
8. Measurement of L,M & K of i) transformer windings and ii) air core coil.
9. OC & SC tests on 3-phase alternator
   a. Predetermine the voltage regulation at various loads and different power factors by EMF method.
10. Load test on single phase transformer
    a. Determine efficiency and regulation at various loads and unity power factor.
11. OC & SC tests on single phase transformer
    a. Determine equivalent circuit parameters
    b. Predetermine efficiency and regulation at various loads and different power factors.
12. Open circuit characteristics of dc shunt generator
    a. Plot OCC of rated speed
    b. Predetermine OCC for other speeds
    c. Determine critical field resistance for a specified speed
    d. Determine critical speed for a specified shunt field resistance

Internal Continuous Assessment (Maximum Marks-50)
60% - Laboratory practical and record
30% - Test/s
10% - Regularity in the class

Semester-End Examination (Maximum Marks-50)
70% - Procedure, conducting experiment, results, tabulation, and inference
20% - Viva voce
10% - Fair record
PTEN09 301: Engineering Mathematics III  
(Common for all branches)

**Teaching scheme**
2 hours lecture and 1 hour tutorial per week

**Objective**
This course provides a quick overview of the concepts and results in complex analysis that may be useful in engineering. Also it gives an introduction to linear algebra and Fourier transform which are wealths of ideas and results with wide area of application.

**Module I: Functions of a Complex Variable**
Functions of a Complex Variable – Limit – Continuity – Derivative of a Complex function – Analytic functions – Cauchy-Riemann Equations – Laplace equation – Harmonic Functions – Conformal Mapping – Examples: $z^n$, $\sin z$, $\cos z$, $\sinh z$, $\cosh z$, $(z+1/z)$– Mobius Transformation.

**Module II: Functions of a Complex Variable**

**Module III: Linear Algebra** - Proofs not required

**Module IV: Fourier Transforms**

<table>
<thead>
<tr>
<th>Text Books</th>
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<tbody>
<tr>
<td><strong>Module I:</strong></td>
</tr>
<tr>
<td>Sections: 12.3, 12.4, 12.5, 12.6, 12.7, 12.9</td>
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<tr>
<td><strong>Module II:</strong></td>
</tr>
<tr>
<td>Sections: 13.1, 13.2, 13.3, 13.4, 14.4, 15.1, 15.2, 15.3, 15.4</td>
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<tr>
<td><strong>Module III:</strong></td>
</tr>
<tr>
<td>Sections: 6.1, 6.2, 6.3, 6.4, 6.7, 6.8, Appendix.B.1</td>
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<tr>
<td><strong>Module IV:</strong></td>
</tr>
<tr>
<td>Sections: 9.1, 9.3, 9.5</td>
</tr>
</tbody>
</table>
**Reference books**


**Internal Continuous Assessment (Maximum Marks-30)**

- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity in the class

**University Examination Pattern**

**PART A:** *Short answer questions (one/two sentences)*

5 x 2 marks=10 marks

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** *Analytical/Problem solving questions*

4 x 5 marks=20 marks

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** *Descriptive/Analytical/Problem solving questions*

4 x 10 marks=40 marks

Two questions from each module with choice to answer one question.

*Maximum Total Marks: 76*
PTEC09 402: ELECTRONIC CIRCUITS

Teaching scheme
3 hours lecture and 1 hour tutorial per week

Objectives
- To impart the basic idea of constructing passive devices
- To develop the skill of analysis and design of various circuits using electronic devices.

Module I
Resistors: concepts of fixed & variable resistors, metal film resistors, wire wound resistors - construction, power rating & tolerance
Capacitors: different types, Construction of mica and ceramic capacitors (disc & tubular), colour code, electrolytic (Teflon) capacitors
Inductors: construction of single layer, multilayer and variable inductors, principle of low power transformers
Diode applications - diode clipping and clamping circuits, voltage multiplier circuits, Rectifiers: Half wave and full wave rectifiers – derivation of rectifier specifications like PIV, DC output voltage, ripple factor, efficiency, rectification factor – analysis of filters with rectifiers – L, C, LC and pi filters
Regulators - zener diode regulator - emitter follower output regulator - series pass transistor feedback voltage regulator - short circuit protection and fold back limiting - load and line regulation curves
BJT circuit models - small signal low frequency and small signal high frequency models of BJT: hybrid model, T model and hybrid π model - effect of temperature on BJT model parameters - equivalent circuits of CC, CB and CE configurations - current gain - voltage gain - input and output impedances
BJT amplifiers: biasing - load line - bias stabilization - stability factor - bias compensation - analysis and design of CC, CE and CB configurations - RC coupled multistage amplifiers - high frequency response

Module II
The amplifier gain function –Low frequency and high frequency responses- Use of open circuit and short circuit time constants in finding the cut-off frequencies-Low and high frequency response of common emitter amplifier -Emitter followers.
Power amplifiers - class A, B, AB, C, D & S power amplifiers - harmonic distortion - efficiency - wide band amplifiers - broad banding techniques - low frequency and high frequency compensation - cascode amplifier - broad banding using inductive loads – Darlington pairs

Module III
Analysis of UJT Characteristics and relaxation Oscillator
JFET – structure and VI characteristics - biasing of JFET -- analyses of common source and common drain amplifier configurations - biasing in ICs
Positive feedback and oscillators - analysis and design of RC phase shift, Wien - bridge, Colpitt’s, Hartley and crystal oscillators - stabilization of oscillations

Teaching scheme Credits: 5
3 hours lecture and 1 hour tutorial per week
Differential Amplifiers - The BJT differential pair - Large and small signal operation - Large and small signal operation - Non ideal characteristics of the differential amplifier - Differential amplifier with active load - concept of CMRR - methods to improve CMRR - Frequency response analysis.

Module IV
Pulse response switching characteristics of a BJT - BJT switches with inductive and capacitive loads - non saturating switches - emitter follower with capacitive loading - Bistable multivibrator – principles & analysis - fixed bias and self biased transistor bistable circuit - triggering methods - Schmitt trigger analysis of emitter coupled circuit.
Monostable and astable multivibrators - collector coupled monoshot - emitter coupled monoshot - triggering the monoshot - collector coupled and emitter coupled astable multivibrator – analysis of sweep circuits - principles of miller and bootstrap circuits

Text Books

Reference Books
9. Spencer & Ghausi, *Introduction to Electronic Circuit Design*; Pearson Education

Internal Continuous Assessment (Maximum Marks 30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity & Participation in the class

Note: One of the assignments shall be simulation of circuits using any SPICE tool.

End-Term Examination

PART A: Short answer questions (one/two sentences) 5 x 2 marks = 10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks = 20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks = 40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70
PTEC09 403: ANALOG COMMUNICATION

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Objectives
- To impart the basic concepts of basic analog modulation schemes
- To develop understanding about performance of analog communication system

Module I

Module II
Introduction to continuous wave modulation-needs of modulation-Amplitude modulators and transmitters (low level, medium & high power)-spectrum-Demodulation.DSBSC signals-spectrum, modulators demodulators.SSB signals-spectrum, modulators demodulators.-VSB-signal and spectra-modulation and demodulation. Signal to Noise ratio in amplitude modulated systems.
Angle modulation-FM &PM (narrowband & wideband)-Transmission bandwidth. Generation of FM (direct indirect methods). De-emphasis& pre-emphasis Signal to Noise ratio in Angle modulated systems

Module III
Receivers for continuous wave modulation-Tuned Radio Frequency Receiver-A detailed study about Super heterodyne Receiver-Special purpose receivers-double conversion receivers-receiver specifications- Frequency Translation, FDM-FM stereo multiplexing-Phase locked loop operation-synchronous detection and frequency synthesis - FM Receiver-threshold effect

Module IV

Text Books
1. Simon Haykin, ‘Communication Systems’ Wiley India, New Delhi,4Ed., 2008

Reference Books
5. Dennis Roddy and John Coolen, ‘Electronic Communication Systems’ PHI
University Examination Pattern

PART A: Short answer questions (one/two sentences)  
5 x 2 marks=10 marks

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions  
4 x 5 marks=20 marks

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions  
4 x 10 marks=40 marks

Two questions from each module with choice to answer one question.

Maximum Total Marks: 70

Internal Continuous Assessment (Maximum Marks-30)

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

Note: One of the assignments shall be simulation of continuous systems using any technical
PTEC09 404: Computer Organization and Architecture

**Teaching scheme**
2 hours lecture and 1 hour tutorial per week

**Credits:** 4

**Objectives**
- *To impart the basic idea of memory & system organisation and architecture of computers*
- *To develop the basic understanding & programming concepts of 8085 microprocessor*

**Module I**
Design methodology- the register level- the processor level components and design-Processor basics-CPU Organization-Instruction set instruction formats-types and programming considerations.Data path design-fixed point arithmetic –various operations-arithmetic & logic units-combinational and sequential ALUs-Control design-Hardwired control-micro programmed control

**Module II**
Memory Organization-memory technology-Device Characteristitcs-Random access memories-serial access memories-Memory systems-multi level memories-Address translation memory allocation-caches-features-address mappings-Structure versus performance

**Module III**
System Organization -comunication methods-basic concepts,bus control-I/O and system control-Programmed I/O DMA  and interrupts; I/O processors-Parallel processing-Processor level parallelism-multiprocessors-shared bus systems

**Module IV**
Introduction to Microprocessor architecture, 8085 architecture, Instruction set, Counter and timing delays, stacks and subroutines, code conversion, Interrupts, basic Interfacing concepts-Memory mapped and I/O mapped I/O

**Text Books**
2. Ramesh S Gaonkar -8085 Architecture and programming, Wiley Eastern

**Reference Books**
2. M Morris Mano; Computer system Architecture, (3rd Edition),PHI /Pe
3. Heuring & Jordan: Computer system Design& Architecture,Addison Wesley
Internal Continuous Assessment *(Maximum Marks-30)*

- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity & Participation in the class

End-Term Examination

**PART A:** *Short answer questions (one/two sentences)*  
5 x 2 marks=10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** *Analytical/Problem solving questions*  
4 x 5 marks=20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** *Descriptive/Analytical/Problem solving questions*  
4 x 10 marks=40 marks  
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTEC09 405 SOLID STATE DEVICES

Teaching scheme

Credits: 4
2 hours lecture and 1 hour tutorial per week

Objectives

- To impart the basic concepts of semiconductor Physics
- To create an insight into the working of different conventional electronic devices

Module I

Energy bands and charge carriers in semiconductors - direct and indirect band gap semiconductors - concept of effective mass - intrinsic and extrinsic semiconductors - Fermi level - electron and hole concentrations at equilibrium - temperature dependence of carrier concentrations - conductivity and mobility - quasi Fermi level - diffusion and drift of carriers - Einstein relation - continuity equation

Module II

PN junctions - contact potential - space charge at a junction - current flow at a junction - carrier injection - diode equation - minority and majority carrier currents - capacitance of pn junctions - reverse bias breakdown - zener and avalanche breakdown - abrupt and graded junctions - schottky barrier - rectifying and ohmic contacts - tunnel diode - varactor diode - zener diode - GaAs isotype diodes - Metal semiconductor junctions-Heterojunctions.

Module III

Bipolar junction transistors-Minority carrier distribution and terminal currents-the coupled diode model-charge control analysis -switching -Drift in the base region,Base narrowing ,Avalanche breakdown,Kirk effect-frequency limitations of transistor -capacitance and charging times-Hetero junction bipolar transistors.

Junction FET - pinch off and saturation - gate control - VI characteristics

Module IV

MOS capacitor - accumulation, depletion and strong inversion - threshold voltage - MOSFET - p channel and n channel MOSFETs - depletion and enhancement mode MOSFETs - substrate bias effects - floating gate MOSFETs - short channel effects

Power Diodes - SCR- Insulated Gate Bipolar Transistor – Power MOSFETs

Text Books

2. Sze S M, Physics of Semiconductor Devices, Wiley India
5. Sima Dimitrijev, Physics of Semiconductor Devices, Oxford University Press

Reference Books

1. Sah C T, Solid State Electronics, World Scientific
2. Neamen, Semiconductor Physics & Devices, Pearson Education
3. Muller & Camins, Device Electronics for Integrated Circuits, John Wiley
4. Dipankar Nagchoudhuri : Microelectronic Devices, Pearson Education
Internal Continuous Assessment (Maximum Marks-30)

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity & Participation in the class

Note: One of the assignments shall be problems involving the theory of devices.

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks = 10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks = 20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks = 40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70

PTEC09 406(P) ELECTRONIC CIRCUITS LAB

Teaching scheme
2 hours practical per week

Credits: 2

1. Rectifiers with C, LC & CLC filters - half wave, full wave & Bridge
2. Clipping, Clamping circuits & voltage multipliers with diodes
3. Series Voltage regulator with short circuit and fold back protection
4. JFET characteristics in CS and CD modes
5. MOSFET characteristics in CS and CD modes
6. Emitter follower with & without complementary transistors - frequency and phase response for a capacitive load
7. RC coupled amplifier - frequency response - with and without feedback
8. UJT characteristics & the relaxation oscillator
9. Phase shift oscillator using BJT
10. Hartley / Colpitts oscillator using BJT
11. Single BJT crystal oscillator
12. Power amplifier - Class A & Class AB
13. Cascode amplifier - frequency response
14. Multivibrators using BJT
15. Sweep Circuits

Note: (i) A minimum of **10** experiments must be conducted
(ii) Each experiment will have two parts – a simulation part (Using SPICE tool) and a hardware realization part

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**Internal Continuous Assessment** *(Maximum Marks-50)*

- 60% - Laboratory practical and record
- 30% - Test
- 10% - Regularity in the class

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**Semester-End Examination** *(Maximum Marks-50)*

- 70% - Procedure, conducting experiment, results, tabulation, and inference
- 20% - Viva voce
- 10% - Fair record

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**PTEC09 407(P) Analog Communication Lab**

**Teaching scheme**

- 2 hours practical per week

**Credits:** 2

1. AM generation
2. AM detection with simple and delayed AGC
3. Balanced modulator for DSB –SC signal
4. Mixer using JFET/BJT
5. FM generation (reactance modulator)
6. FM demodulation
7. PAM generation and demodulation
8. Implementation of intermediate frequency amplifier
9. FM demodulation using PLL
10. AM generation and demodulation using opamps/IC multipliers
11. SSB generation and demodulation using integrated circuits

**Note:** A minimum of 10 experiments must be conducted

**Internal Continuous Assessment (Maximum Marks-50)**

- 60% - Laboratory practical and record
- 30% - Test/s
- 10% - Regularity in the class

**Note:** A term project, comprising of an application oriented electronic circuit, is to be designed and completed as part of this practical subject.

**Semester End Examination (Maximum Marks-50)**

- 70% - Procedure, conducting experiment, results, tabulation, and inference
- 20% - Viva voce
- 10% - Fair record

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**PTEC09 501: Digital Signal Processing**

**Teaching scheme**

Credit: 5

2 hours lecture and 1 hour tutorial per week

**Objectives**

To impart basic ideas (i) in the transform used in digital domain (ii) in the design and hardware realization of digital filters

**Module I**

Review of Discrete Fourier series and Discrete Time Fourier Transform-Frequency domain sampling- Discrete Fourier Transform-Properties-Circular convolution-Linear convolution using DFT-Linear filtering of long data sequences- Overlap add and overlap save methods- Computation of DFT-Decimation in Time and Decimation in Frequency algorithms

**Module II**

Structures for realization of discrete time systems-Signal flow graph representation-structures for FIR and IIR systems-direct form, cascade form, parallel form-lattice and transposed structures-representation of numbers & errors due to rounding and truncation-Quantization of filter coefficients-round off effects in digital filters-Limit cycle oscillations, scaling to prevent overflow.

**Module III**

Module IV
Computer Architectures for signal processing-Harvard Architecture, Pipelining,Multiplier-Accumulator, Special Instructions for DSP, extended parallelism-General Purpose DSP Processors-Implementation of DSP Algorithms for various operations-Special purpose DSP hardware-Hardware Digital filters and FFT processors-Case study and overview of TMS320 series processor, ADSP 21XX processor

Text Books

Reference Books
2. B Venkataramani & M.Bhaskar, Digital Signal Processors-Architecture,
3. Programming and Applications, Tata Mcgraw Hill

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

Note: One of the assignments shall be simulation of filters using MATLAB

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.
PTEC09 502: QUANTITATIVE TECHNIQUES FOR MANAGERIAL DECISIONS

Teaching scheme
1 hour lecture and 1 hour tutorial per week

Credits: 4

Objectives
To impart basic ideas on various quantitative techniques for managerial decision making

Module 1
Decision making- strategic and tactical decisions-strategy formulation-models of decision making-single stage decisions under risk-incremental analysis-multistage decision making-decision trees-decision making under uncertainty- baye’s decision theory-

Module 2
Inventory control-functions of inventory-structure of inventory problems-relavant cost-opposing costs-selective control techniques-dynamic inventory models under certainty-calssical EOQ model with and without back logging-production lot size model-quantity discount- safety stock-probabilistic model-one time mode-P system and Q system.

Module 3
Statement of the LP problem- slack and surplus variables-basic feasible solutions- reduction of a feasible solution to basic feasible solution-artificial variable-optimality conditions- unbounded solutions-charnes ’ M method-two phase method-degeneracy-duality.

Module 4
Transportation problem- coefficient matrix and its properties-basic set of column vectors-linear combination of basic vectors-tableau format-stepping stone algorithm-UV method-inequality constraints-degeneracy in transportation problems - assignment problem-hungarian method

Reference Books

1. Hadley.G Linear programming, Addison Wesley
4. Weist & Levy , A management Guide to PERT and CPM. Prentice hall of India
5. Starr & Miller , Inventory control –Theory and Practice- Prentice Hall of India
### Internal Continuous Assessment *(Maximum Marks-30)*

- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity in the class

### University Examination Pattern

**PART A:** Short answer questions (one/two sentences)  
5 x 2 marks = 10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions  
4 x 5 marks = 20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions  
4 x 10 marks = 40 marks  
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTEC09 503: Electromagnetic Field Theory

Objectives

- To impart the knowledge of electric, magnetic fields and the equations governing them as well as time varying field
- To develop understanding about guided waves & transmission lines

Module I


Static electric & Magnetic field: Electrical scalar potential- different types of potential distribution- Potential gradient- Energy stored-Boundary conditions Capacitance-Steady current and current density in a conductor-Equation of continuity- energy stored in magnetic fields- Magnetic dipole- Electric and Magnetic boundary conditions- vector magnetic potential-Magnetic field intensity.

Module II

Maxwell’s equations and travelling waves: conduction current and displacement current- Maxwell’s equations- Plane waves- Poynting theorem and Poynting vector- Plane electromagnetic waves- Solution for free space condition- Uniform plane wave-wave equation for conducting medium- Wave polarization- Poisson’s and Laplace equations.

Module III

Guided waves between parallel planes- transverse electric and transverse magnetic waves and its characteristics-, linear elliptical and circular polarization, wave equations for conducting medium, wave propagation in conductors and dielectric, depth of penetration, reflection and refraction of plane waves by conductor and dielectric, Poynting vector and flow of power

Module IV

Transmission lines & Waveguides: -Transmission line equations- transmission line parameters- Skin effect- VSWR- Characteristic impedance- Stub matching- Smith chart - Phase velocity and group velocity Theory of waveguide transmission-Rectangular waveguides- TE modes-TM modes- mathematical analysis- circular wave guide- modes of propagation- dominant modes- cut off wave length cavity resonators-applications

Text Books


Reference Books

**Internal Continuous Assessment (Maximum Marks-30)**

- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity in the class

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**University Examination Pattern**

**PART A:**  *Short answer questions (one/two sentences)*  

5 x 2 marks = 10 marks

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:**  *Analytical/Problem solving questions*  

4 x 5 marks = 20 marks

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:**  *Descriptive/Analytical/Problem solving questions*  

4 x 10 marks = 40 marks

Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTEC09 504: DIGITAL COMMUNICATION

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Objectives
- To impart the basic concepts of various digital modulation schemes
- To develop understanding about digital transmitters & Receivers

Module I
Analog pulse modulation-generation and demodulation of PAM/TDM Systems, PPM and PWM- Sampling theorem for band limited and band pass signals-Signal Reconstruction-Practical difficulties in Signal Reconstruction- PCM- Quantization-Signal to noise ratio for quantized pulses-uniform and nonuniform quantization-comparing-μ Law and A Law characteristics-DPCM,Delta modulation, Adaptive Delta modulation-Line codes-NRZ,RZ,Phase encoded, Multilevel binary

Module II
Pulse shaping-Inter symbol interference-Nyquist’s Criterion for distortion less Base Binary Transmission-Signaling with duobinary pulses -eye diagram-Equalizer-Transversal Equalizer- Zero forcing Equalizer-Decision Feedback Equalizer-Preset and Adaptive Equalizer- Scrambling and descrambling- Geometric Representation of Signals-Schwarz inequality-Gram-Schmidt Orthogonalization Procedure

Module III
Optimum receiver-Conversion of continuous AWGN channel into a vector channel-Likelihood Functions-Maximum Likelihood receiver--Matched filter-correlation receiver-decision procedure- Optimum receiver of colored noise-carrier and symbol synchronization-Fundamental concepts of spread spectrum systems-pseudo noise sequence-performance of direct sequence spread spectrum systems-analysis of direct Sequence spread spectrum systems- the prosing gain and anti jamming margin-frequency hopped spread spectrum systems –time hopped spread spectrum systems-time synchronisation

Module IV
Digital modulation schemes- coherent binary schemes-ASK, FSK, PSK, MSK and coherent M- ary schemes –calculation of average probability error for different modulation schemes-power spectra of digitally modulated signals-performance comparison for different modulation schemes

Text Books
2. Bernard Sklar,’Digital Communication’ Pearson education

Reference Books
1. Simon Haykin, ‘Digital Communication’, Wiley India
2. Bruce Carlson, Communication Systems; McGraw Hill
Objectives

- To introduce the student with knowledge about architecture, interfacing and programming with 8086 microprocessors and 8051 microcontrollers. It gives a brief introduction to ARM 7 and ARM 9 micro controllers.
- With this paper, the student should be able to design microprocessor/controller based system for any relevant applications.

Module I
Software architecture of the 8086/8088 microprocessors-Address space, Data organization, registers, memory segmentation and addressing, stack, I/O space, Assembly language programming and program development.
Module II
8086/88 microprocessor architecture-min/max mode- Coprocessor and Multiprocessor configuration - hardware organization of address space-control signals and I/O interfaces-
Memory devices, circuits and sub system design – various types of memories, wait state and system memory circuitry.

Module III
I/O interfacing circuits –Hand shaking, serial and parallel interfacing-Address decoding-
Interfacing chips-Programmable peripheral interfacing (8255)-Programmable communication interface(8251)-Programmable timer(8253)-DMA controller(8237/8257)-Programmable interrupt controller(8259)-Keyboard display interface(8279)

Module IV
Intel 8051 microcontroller-CPU operation-Memory space-Software overview-Peripheral overview-Interrupt- timers parallel port inputs and outputs-serial port-low power special modes of operation-Introduction to ARM processors –features of ARM 7 and 9 processors

Text Books
4. Andrew .N.Sloss,Dominic Sysmes,Chris Wright - Arm System Developers Guide
   Designing and Optimizing System software, Morgan Kaufmann Publishers.

Reference Books
1. 1.Intel Data Book vol.1, Embedded Microcontrollers and Processors
2. Hall D.V., Microprocessors and Interfacing McGraw Hill
3. Mohammed R.,Microprocessor& Microcomputer based system design,Universal Book stall

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class
University Examination Pattern

**PART A:** Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*

**PTEC09 506: LINEAR INTEGRATED CIRCUITS**

**Teaching scheme**
1 hour lecture and 1 hour tutorial per week

**Credits:** 3

**Objectives**
To develop the skill of analysis and design of various circuits using operational amplifiers

• To develop design skills to design various circuits using different data conversion systems

**Module I**
Various stages of an operational amplifier - simplified schematic circuit of op-amp 741 - need for compensation - lead, lag and lead-lag compensation schemes - typical op-amp parameters - slew rate - power supply rejection ratio - open loop gain - unity gain bandwidth - offset current & offset voltage

**Module II**
Signal generators-Phase shift and Wien Bridge Oscillators-Astable and Monostable Circuits-Linear sweep circuits.
Active filters-filter transfer function-Butterworth and Chebyshev filters-First order and second order function for low-pass high-pass band-pass band-stop and all-pass filters- Sallen-key LPF
HPF-Delyannis-Friend band Pass filters-twin –tee notch filter-Second order LCR Resonator and realizations of various types-Filters based on inductor replacement-switched capacitor filters

Module III
Timer IC 555 – internal diagram – working - multivibrators with timer IC 555
Data converters - definitions and specifications – DAC - Weighted resistor and R-2R DAC-Bipolar DAC
ADC - flash, integrating type, Counter Ramp, pipeline, tracking and Successive approximation, dual slope & oversampling ADCs - sigma - delta ADC
Linear voltage regulators- protection mechanisms-LM 723 Functional-diagram-Design of voltage regulator using 723-Three terminal Voltage regulators-functional operation of 78xx series IC and design of fixed and adjustable regulators

Module IV
Phase locked loops- operation of first and second order PLLs-Lock and Capture range-LM565PLL-Application of PLL as AM/FM/FSK/ detectors, frequency translators, phase shifter, tracking filter, signal synchronizer and frequency synthesizer. Voltage controlled oscillator

Text Books
1. Sergio Franco , Design with Operational Amplifiers& Analog integrated Circuits ; McGraw Hill
3. Fiore J.M., Operational Amplifiers and Linear Integrated Circuits, Jaico Publishing House
4. Gayakwad, Operational Amplifiers, Jaico Publishing House

Reference Books
2. Schumann & Valkenberg, Design of Analog Filters, Oxford University Press

Internal Continuous Assessment (Maximum Marks-30)

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity & Participation in the class

Note: One of the assignments shall be simulation of circuits using any SPICE tool.
End-Term Examination

**PART A:** Short answer questions (one/two sentences)  
5 x 2 marks = 10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions  
4 x 5 marks = 20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions  
4 x 10 marks = 40 marks  
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*

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**PTEC09 507(P) MICROPROCESSORS & MICROCONTROLLERS LAB**

**Teaching scheme**

2 hours practical per week  

Credits: 2

1. 8086 kit familiarization and basic experiments  
2. Programming exercise using BCD and Hexadecimal numbers  
3. Programming exercise: sorting, searching and string  
4. Interfacing with A/D and D/A converters  
5. Interfacing with stepper motors  
6. IBM PC programming: Basic programs using DOS and BIOS interrupts  
7. Interfacing with PC: Serial communication and Parallel printer interfacing

**Interfacing experiments using 8051**

1. Parallel interfacing I/O ports (Matrix keyboards)  
2. Serial communication with PC  
3. Parallel interfacing – LCD  
4. Interfacing with serial EEPROM

**Note:** A minimum of 10 experiments must be conducted
PTEC09 508(P) LINEAR INTEGRATED CIRCUITS LAB

Teaching scheme

2 hours practical per week
1. Measurement of op-amp parameters-CMRR, slew rate, open loop gain, input and output impedances
2. Inverting and non inverting amplifiers, integrators, and differentiators - Frequency response, Comparators-Zero crossing detector Schmitt trigger-precision limiter
3. Instrumentation amplifier-gain, CMRR & input impedance
4. Single op-amp second order LFF and HPF - Sallen-Key configuration Narrow band active BPF - Deliyannis configuration
5. Active notch filter realization using op-amps
6. Wein bridges oscillator with amplitude stabilization
7. Generation and demodulation of PWM and PPM
8. Multipliers using op-amps - 1, 2 & 4 quadrant multipliers
9. Square, triangular and ramp generation using op-amps
10. Astable and monostable multivibrators using op-amps
11. Log and Antilog amplifiers
12. Voltatage regulation using IC 723
13. Astable and monostable multivibrators using IC 555
14. Design of PLL for given lock and capture ranges & frequency multiplication
15. Applications using PLL
16. Realisation of ADCs and DACs

Note: A minimum of 10 experiments must be conducted

Internal Continuous Assessment (Maximum Marks-50)

60% - Laboratory practical and record
30% - Test/s
10% - Regularity in the class

Semester-End Examination (Maximum Marks-50)

70% - Procedure and tabulation form, Conducting experiment, results and inference
20% - Viva voce
10% - Fair record
PTEC09 601: VLSI DESIGN

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 5

Objectives

- To study the issues in devices used for VLSI design
- To introduce the various building blocks and test methods in a digital integrated circuit design
- To introduce the various steps in IC fabrication, starting from the raw material to the finished product as well as physical principles involved in these processes

Module I

Short and narrow channel effects in MOS transistor (MOST) – sub threshold conduction – body effect - channel length modulation - drain induced barrier lowering - hot carrier effects - velocity saturation of charge carriers

Scaling of MOST - constant voltage and constant field scaling - digital MOSFET model - Estimation of interconnect parasitics and calculation of interconnect delay.

MOS inverters - resistive load, Saturated NMOS load, Depletion NMOS load, pseudo MOS - CMOS inverters-robustness and performance – capacitance components - charge sharing – buffer design – power dissipation - CMOS ring oscillator

Module II

CMOS logic Styles - clocking strategies - Design & implementation of Adder – Full adder, Dynamic adder, Carry bypass adder, Carry select adder, Square root carry selector adder, Carry look head adder - Multipliers, and array multipliers - Multiplexers - Memory elements- SRAM, DRAM, ROM, Sense amplifiers – Differential, Single ended - Reliability and testing of VLSI circuits – General concept, CMOS testing - Test generation methods

Module III


Module IV

Device isolation-contacts and metallization-junction and oxide isolation –LOCOS- SILO-SWAMI process-trench isolation – silicon on insulator isolation - schottky contacts-implanted ohmic contacts-alloyed contacts-refractory metal contact technology-multi level metallization

CMOS and bipolar technologies – early bipolar process-advanced bipolar processes CMOS- p well process – twin tub process

Layout and design rules ( λ and μ based) - layout using cell hierarchy - layout of MOSFET – stick diagram - layout of the inverter, NOR and NAND gates – Layout guide lines
### Text Books
1. Weste & Harris, *CMOS VLSI Design*, Pearson Education
2. Plummer, Deal & Griffin, *Silicon VLSI Technology*, Pearson Education

### Reference Books

### Internal Continuous Assessment *(Maximum Marks-30)*
- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity in the class

### End-Term Examination

**PART A:** Short answer questions (one/two sentences)  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.  
5 x 2 marks = 10 marks

**PART B:** Analytical/Problem solving questions  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.  
4 x 5 marks = 20 marks

**PART C:** Descriptive/Analytical/Problem solving questions  
Two questions from each module with choice to answer one question.  
4 x 10 marks = 40 marks

*Maximum Total Marks: 70*
Objectives

- To impart the basic concepts of radiating structures and their arrays
- To give understanding about analysis and synthesis of arrays
- To give idea about basic propagation mechanisms

Module I

Retarded potentials: Radiation, retarded potential - Radiation from an A.C current element-monopoles and dipoles-power radiated from a dipole

Antenna Parameters: Introduction, Isotropic radiators, Radiation pattern, Gain -radiation intensity- Directive gain, Directivity, antenna efficiency Reciprocity theorem & its applications, effective aperture, radiation resistance, terminal impedance, noise temperature, elementary ideas about self & mutual impedance, front-to-back ratio, antenna beam width, antenna bandwidth, antenna beam efficiency, antenna beam area or beam solid angle, polarization, antenna temperature.

Module II

Antenna Arrays: Introduction, various forms of antenna arrays, arrays of point sources, nonisotropic but similar point sources, multiplication of patterns, arrays of n-isotropic sources of equal amplitude and spacing (Broad-side & End-fire array cases), array factor, directivity and beam width, array of n-isotropic sources of equal amplitude and spacing end-fire array with increased directivity, scanning arrays, Dolph-Tchebysceff arrays, tapering of arrays, binomial arrays, continuous arrays, rectangular arrays, superdirective arrays.

Module III

VLF, LF and MF antennas: Introduction, effects of ground on antenna performance, effects of antenna height, efficiency of electrically short antenna, medium frequency antennas, high frequency antennas, fundamental antenna (i.e. half wave dipole or dipole antenna), long wire antenna, V and inverted V antenna, Rhombic antenna, traveling wave antenna, radio direction finders, loop antennas, VHF, UHF, SHF Antennas: Introduction. Folded dipole antenna, Yagi-Uda antenna, and helical antenna, slot antenna, microstrip or patch antennas, andturnstile antenna, frequency independent antennas- log periodic antenna, and microwave antennas- Microstrip antenna, fractal antenna.

Module IV

Factors involved in the propagation of radio waves: the ground wave-Reflection of radio waves by the surface of the earth-space wave propagation-considerations in space wave propagation-atmospheric effects in space wave propagation-ionosphere and its effects on radio waves -mechanism of ionosphere propagation-refraction and reflection of sky waves by ionosphere-ray paths-skip distance-maximum usable frequency-vertical and oblique incidence-fading of signals - selective fading-diversity reception, Duct Propagation.
Text Books

1. Electromagnetic waves & Radiating Systems— Jordan & Balman, Prentice Hall India

Reference Books


Internal Continuous Assessment (Maximum Marks-30)

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70
Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives
• To impart the basic theory behind the analysis of continuous and discrete Systems in time and frequency domains
• To introduces concepts about the state space modelling of systems.

Module I

Module II
1. Time Domain analysis:
   Analysis of Continuous Time systems-Transient and steady State Responses-Standard Test Signals-Response comparisons for various Root locations in the S-plane-Time Domain Solutions of First order systems- Step Response of Second order system –Time domain specifications – Relationships between Damping ratio and the amount of Overshoot for a second Order system
   • Effects of derivative and Integral Control on the Transien
   • Performance of feedback Control systems.
   • Steady state Response-steady state error –computations of S. S
   • Error –error constants.
   • Construction of root locus.
2. Frequency Domain Analysis:

Module III
Modeling of discrete-time systems-sampling-mathematical derivations for sampling-sample and hold-Z transforms- properties-solutions of difference
Equations using Z-transforms-example of sampled data systems –mapping between s plane and z plane –cyclic and multi-rate sampling (definitions only) –analysis of discrete time systems-pulse transfer function-examples-stability –Jury’s criterion –bilinear transformation-stability analysis after bilinear transformation –stability analysis Routh-Hurwitz techniques-
Module IV
State Space Analysis: Introduction-Definitions and explanations of the terms STATE, STATE VARIABLES, STATE VECTOR AND STATE SPACE-State Space Representations of Linear Time-invariant System with i) single input and output ii) multi variable systems iii) SISO System in which forcing Function involves-Eigen values-phase variable and Diagonal forms-Invariance of Eigen values under linear transformation-Diagonilisation Solutions of Linear Time-invariant State Equations-Homogeneous and Non-homogeneous case(example up to second order only)- Matrix Exponential- Laplace Transform approach to the solutions of state equations-State Transition Matrix-properties. State Space representation of Discrete Time Systems-Relation between Transfer function /Transfer Matrix and State Space models for continuous and discrete cases.

Text Books

1. Ogata K. “Modern Control Engineering”, Prentice Hall of India
3. B.C Kuo.,” Automatic Control System”, Prentice Hall of India
4. Nagarath I. J & Gopan M.,”Control System Engineering”,Wiley India Ltd

Reference Books

2. Dorf R.C& Bishop R.H.,Modern Control Systems”,Addison Wesley

Internal Continuous Assessment (Maximum Marks-30)

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity & Participation in the class
University Examination

**PART A:** Short answer questions (one/two sentences)  
5 x 2 marks = 10 marks

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions  
4 x 5 marks = 20 marks

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions  
4 x 10 marks = 40 marks

Two questions from each module with choice to answer one question.

Maximum Total Marks: 70

Note: More than 75% of the questions shall be analytical/problem oriented types

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**PTEC09 605: Optical Communication**

**Teaching scheme**

Credits: 3

1 hour lecture and 1 hour tutorial per week

**Objectives**

- To provide the basic theory of optical fibres and principle of various components in optical communication system.
- To give basic idea about system aspects and design concepts of fiber optical system

**Module I**

Solution to Maxwell’s equation in a circularly symmetric step index optical fiber – single mode and multimode fibres – concept of V number – graded index fibres – polarization maintaining fibres – attenuation mechanisms in fibres – dispersion in single mode and multimode fibres – dispersion shifted and dispersion flattened fibres

**Module II**


**Module III**

Intensity modulated direct detection systems – quantum limit to receiver sensitivity – detected signal & shot noise – ISI and equalization – coherent systems – homodyne and heterodyne systems – system
structures- degradation due to fiber dispersion-degradation induced by non-linear effects in fiber propagation

Module IV
Optical amplifiers-semiconductor amplifier-rate earth doped fiber amplifier (with special reference to erbium doped fibers) – broad band EDFA Raman amplifier-Brillouin amplifier-principles of operation-, WDM & DWDM Optical System, Optical Networks – SONET/SDH

Text Books
2. John M.Senior . ‘Optical Fiber Communication Principles & Practice’ ,PHI Publication

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

PART A:  Short answer questions (one/two sentences)  5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B:  Analytical/Problem solving questions  4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C:  Descriptive/Analytical/Problem solving questions  4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70
Digital Communication Expts

1. Sampling and reconstruction of low pass signals
2. PCM generation
3. Differential PCM generation
4. Implementation of Delta modulator and demodulator
5. Implementation of line coding schemes; bipolar, Manchester and differential codes
6. Matched filter receiver for rectangular pulse
7. Generation and detection of BASK and BFSK signals
8. Generation and detection of QAM using IC multipliers
9. Performance evaluation of digital modulation & demodulation systems using matlab
10. Active filter design using pspice

Experiments using matlab/DSP kit

1. To find DFT of a given sequence using DIT & DIF FFT algorithms.
2. To find IFFT of a given sequence using DIT & DIF FFT algorithms.
3. IIR filter-low pass & high pass
4. FIR filter-low pass & high pass
5. DSP experiments on processor kit

Note: A minimum of 10 experiments must be conducted

<table>
<thead>
<tr>
<th>Internal Continuous Assessment (Maximum Marks-50)</th>
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<tbody>
<tr>
<td>60% - Laboratory practical and record</td>
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<td>30% - Test/s</td>
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<th>Semester-End Examination (Maximum Marks-50)</th>
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<tbody>
<tr>
<td>70% - Procedure and tabulation form, Conducting experiment, results and inference</td>
</tr>
<tr>
<td>20% - Viva voce</td>
</tr>
<tr>
<td>10% - Fair record</td>
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PTEC09 608(P) MINI PROJECT

Teaching scheme

Credits: 2
2 hours practical per week

Objectives

- To estimate the ability of the student in transforming the theoretical knowledge studied so far into a working model of an Electronic system.
- For enabling the students to gain experience in organisation and implementation of a small project and thus acquire the necessary confidence to carry out main project in the final year.

In this practical course, each group consisting of three/four members is expected to design and develop a moderately complex electronic system with practical applications; this should be a working model. The basic concepts of product design may be taken into consideration while designing the project. A committee consisting of minimum three faculty members specialised in Electronics Engineering will perform assessment of the mini project. Students have to submit a report on the mini project and demonstrate the mini project before the evaluation committee.

60% of the total marks to be awarded by the guide/Co-ordinator and the remaining 40% by the evaluation committee.

<table>
<thead>
<tr>
<th>Internal Continuous Assessment (50 marks)</th>
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<tbody>
<tr>
<td>40% - Design and development</td>
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<tr>
<td>30% - Final result and Demonstration</td>
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<tr>
<td>20% - Report</td>
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<tr>
<td>10% - Regularity in the class</td>
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<tr>
<th>Semester-End Examination (Maximum Marks-50)</th>
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<tbody>
<tr>
<td>20% - Demonstration of mini project</td>
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<tr>
<td>50% - Practical test connected with mini project</td>
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<tr>
<td>20% - Viva voce</td>
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<td>10% - Fair record</td>
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PTEC09 L01: POWER ELECTRONICS

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives

- To introduce the basic components and systems used in power electronics.
- To impart the ideas of design and analysis of typical power electronics system

Module I
Power diodes-basic structure and V-I characteristics-various types-power transistors-BJT,MOSFET and IGBT-basic structure and V-I characteristics-thyristors-basic structure –static and dynamic characteristics –device specifications and ratings- methods of turning on-gate triggering circuit using UJT-methods of turning off-communication circuits-TRIAC

Module II
Line frequency phase controlled rectifiers using SCR-single phase rectifier with R and RL loads- half controlled and fully controlled converters with continuous and constant currents-SCR inverters-Circuits for single phase inverters-series parallel and bridge inverters- pulse width modulated inverters- basic circuit operation

Module III
AC regulators –single phase ac regulator with R and RL loads-sequence
Control of ac regulators-cycloconverter-basic principle of operation-single phase to single phase cycloconverter-choppers-principle of operation-step-up and step-down choppers-speed control of DC motors and induction motors

Module IV
Switching regulators –buck regulators-boost regulators –buck-boost regulators-cuk regulators-switched mode power supply-principle of operation and analysis-comparison with linear power supply –uniinterruptable power supply –basic circuit operation-different configurations-characteristics and applications

Text Books
1. Ned Mohan et., Power Electronics, John Wiley

Reference Books
3. Dewam& Strauhen,Power Semiconductor Circuits,John Wiley
Objective

The main goal of numerical analysis is to find approximate solutions to complex problems using simplest operations of arithmetic. It is a business of solving hard problems by doing a number of easy iterations. The basic objective of this course is to give the students an opportunity to hone their skills in problem solving using numerical methods and to acquaint the students with the potentials of numerical methods for solving various problems arising in engineering.

Module I


Internal Continuous Assessment *(Maximum Marks-30)*

<table>
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<tr>
<th>Percentage</th>
<th>Component</th>
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<tr>
<td>60%</td>
<td>Tests (minimum 2)</td>
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<tr>
<td>30%</td>
<td>Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.</td>
</tr>
<tr>
<td>10%</td>
<td>Regularity in the class</td>
</tr>
</tbody>
</table>

University Examination Pattern

**PART A:** Short answer questions (one/two sentences)  
5 x 2 marks = 10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions  
4 x 5 marks = 20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions  
4 x 10 marks = 40 marks  
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*

**PTEC09 L02: Numerical Methods for Engineers**

**Teaching scheme**

2 hours lecture and 1 hour tutorial per week

**Credits:** 4
Module II
Solutions of system of linear algebraic equations - Gauss elimination methods - Crout’s triangularization methods - Gauss - seidel iteration method - relaxation methods - power method for the determination of Euigen values - polynomial interpolation – Lagrange’s interpolation polynomial - divided differences - Newton’s divided differences interpolation polynomial - finite differences - Gregory - Newton - forward and backward differences interpolation formula (elementary treatment only).

Module III

Module IV
Solution of linear difference equations with constant coefficients - numerical solutions of boundary value problems in ordinary differential equations - finite difference methods for solving two-dimensional Laplace’s equations for a rectangular region - finite difference method of solving heat equation and wave equation with given initial and boundary conditions.

Reference books

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class
### University Examination Pattern

**PART A:** Short answer questions (one/two sentences) \[5 \times 2 \text{ marks}=10 \text{ marks}\]

- All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions \[4 \times 5 \text{ marks}=20 \text{ marks}\]

- Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions \[4 \times 10 \text{ marks}=40 \text{ marks}\]

- Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*

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### EC09 L03: Entrepreneurship

#### Objectives

*To give the basic idea of Entrepreneurship perspectives*

#### Module I

- Entrepreneurial perspectives - understanding of entrepreneurship process - entrepreneurial decision process - entrepreneurship and economic development - characteristics of entrepreneur - entrepreneurial competencies - managerial functions for enterprise

#### Module II

- Process of business opportunity identification and evaluation - industrial policy - environment - market survey and market assessment - project report preparation - study of feasibility and viability of a project - assessment of risk in the industry

#### Module III

- Process and strategies for starting a venture - stages of small business growth - entrepreneurship in international environment - entrepreneurship achievement motivation - time management - creativity and innovation structure of the enterprise - planning, implementation and growth

#### Module IV

- Technology acquisition for small units - formalities to be completed for setting up a small scale unit - forms of organizations for small scale units - financing of project and working capital - venture capital
and other equity assistance available – break even analysis and economic ratios technology transfer and business incubation

Reference Books
3. Rao T. V & Deshpande M. V., Prayag Metha & Nadakarni M. S., Developing
5. Dr. Patel V. G., Seven Business Crisis, Tata Mc Graw Hill

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks = 10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks = 20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks = 40 marks
Two questions from each module with choice to answer one question.
PTEC09 L04: SPEECH AND AUDIO PROCESSING

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives

- To impart knowledge about efficient algorithms/techniques for speech coding/compression, synthesis and recognition.

Pre-requisite

EC09 501 Digital Signal Processing

Module I


Module II

Spectral analysis of speech-short time fourier anlysis filter bank design-speech coding-subband coding of speech –transform coding-channel vocoder-formant vocoder-cepstral vocoder-vector quantizer coder

Module III

Speech synthesis –pitch extraction algorithms-Gold Rabiner pitch trackers-autocorrelation pitch trackers- homomorphic speech processing-homomorphic systems for convolution-complex cepstrums- Automatic speech recognition system –isolated word recognition-connected word recognition-large vocabulary word recognition systems-pattern classification-DTW,HMM-speaker recognition systems- speaker verification systems-speaker identification systems

Module IV

Audio Processing: Auditory perception and psychoacoustics - Masking, frequency and loudness perception, spatial perception, Digital Audio, Audio Coding - High quality, low-bit-rate audio coding standards, MPEG, AC-3, Multichannel audio - Stereo, 3D binaural and Multichannel surround sound.

Text Books


Reference Books

PTEC09 L05: Satellite Communication

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives
- To impart the basic concepts of satellite communication and systems
- To develop understanding about the link design and the latest trends in satellite communication

Module I

Satellite Orbits: Orbital mechanics-Kepler’s laws, locating the satellite in orbit, orbital elements; look angle determination-subsatellite point, azimuth and elevation angle calculation; orbital perturbations-longitudinal and inclination changes; launches and launch vehicles-ELVs, placing satellites into geostationary orbit; orbital effects in communication system performance-doppler shift, range variations, solar eclipse, sun transit outage

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70
Module II

**Communication Satellites**- Satellite subsystem; Attitude and orbit control system (AOCS); Telemetry, Tracking, Command and Monitoring (TTC&M); power systems; communications subsystem—description, transponders; satellite antennas—basic antenna types, satellite antennas in practice

Module III

**Satellite link design and Satellite access**- Basic transmission theory, system noise temperature and G/T ratio; Downlink design—link budget; Uplink design; design for specified C/N, uplink and downlink attenuation in rain, communication link design procedure; system design examples.

Module IV

Multiple access schemes—FDMA, TDMA, CDMA, DAMA; VSAT systems—basic techniques, VSAT earth station engineering, system design; DBS systems—C-band and Ku-band home TV, digital DBS; satellite mobile systems; GPS

Text Books


Internal Continuous Assessment *(Maximum Marks—30)*

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

**PART A:** Short answer questions (one/two sentences) 5 x 2 marks=10 marks

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions 4 x 5 marks=20 marks

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks

Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
Section 1: Engineering Economics

Objective

*Impart fundamental economic principles that can assist engineers to make more efficient and economical decisions.*

Module I


Module II


Text books


**Internal Continuous Assessment (Maximum Marks-15)**

- 60% - Tests (minimum 2)
- 30% - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- 10% - Regularity in the class
Section 2: Principles of Management

Objective

- To provide knowledge on principles of management, decision making techniques, accounting principles and basic management streams

Module I

Principles of management – Evolution of management theory and functions of management
Organizational structure – Principle and types. Decision making – Strategic, tactical & operational decisions, decision making under certainty, risk & uncertainty and multistage decisions & decision tree
Human resource management – Basic concepts of job analysis, job evaluation, merit rating, wages, incentives, recruitment, training and industrial relations

Module II


University Examination Pattern – for Section 1

Note: Section 1 and Section 2 are to be answered in separate answer books

PART A: Short answer questions (one/two sentences) 2 x 2 marks = 4 marks 1 x 1 mark = 1 mark
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 2 x 5 marks = 10 marks
Candidates have to answer two questions out of three. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 2 x 10 marks = 20 marks
Two questions from each module with choice to answer one question.
References
1. F. Mazda, Engineering management, Addison Wesley, Longman Ltd., 1998
2. Lucy C Morse and Daniel L Babcock, Managing engineering and technology, Pearson Prentice Hall
8. Weist and Levy, A Management guide to PERT and CPM, Prantice Hall of India

Internal Continuous Assessment (Maximum Marks-15)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern – for Section 2
Note: Section 1 and Section 2 are to be answered in separate answer books

PART A: Short answer questions (one/two sentences) 2 x 2 marks=4 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 2 x 5 marks=10 marks
Candidates have to answer two questions out of three. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 2 x 10 marks=20 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 35
**PTEC09 701: Information Theory and Coding**

**Teaching scheme**
2 hours lecture and 1 hour tutorial per week

**Credits: 5**

**Objectives**
- To provide basic concepts of Information
- To enable the students to propose, design and analyse suitable coding/decoding scheme for a particular digital communication application

**Module I**
Information theory- information and entropy-properties of entropy of a binary memory less source - extension of a binary memory less source – source coding theorem-Shannon fano coding-Huffman coding –Lempel ziv coding-discrete memoryless source-binary symmetric channel –mutual information-properties-channel capacity –channel coding theorem

**Module II**
Introduction to algebra-groups-fields-binary field arithmetic-construction of Galois field-basic properties-computations-vector spaces-matrices-BCH codes-description-coding & decoding –Reed Solomon codes-coding & decoding

**Module III**
Coding –linear block codes-generator matrices-parity check matrices-encoder-syndrome and error correction-minimum distance-error correction and error detection capabilities-cyclic codes-coding and decoding

**Module IV**

**Text Books**
1. Simon Haykins, Communication Systems, John Wiley

**Reference Books**
1. Das J, Malik A.K., Chatterjee P. K., Principles of Digital Communications, New Age International
2. Simon Haykin, Digital Communications, John Wiley
4. Tomasi, Electronic Communications, Fundamentals Through Advanced, Pearson education
5. Sklon, Digital Communications Pearson education
**Internal Continuous Assessment** *(Maximum Marks-30)*

- 60% - Tests (minimum 2)
- 30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- 10% - Regularity in the class

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**University Examination Pattern**

**PART A:** *Short answer questions (one/two sentences)*

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

5 x 2 marks = 10 marks

**PART B:** *Analytical/Problem solving questions*

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

4 x 5 marks = 20 marks

**PART C:** *Descriptive/Analytical/Problem solving questions*

Two questions from each module with choice to answer one question.

4 x 10 marks = 40 marks

*Maximum Total Marks: 70*
Objectives

- To give the basic ideas about the characteristics and applications of microwave frequency bands
- To understand the working of various microwave passive and active devices and circuits.

Module I
Characteristic, features and applications of microwaves- Scattering matrix representation of microwave networks, properties of scattering matrices, properties and S-matrices for typical network such as section of uniform transmission line, 3-port networks (reciprocal and nonreciprocal), T-junctions directional coupler, magic tee, ferrite devices, isolator, circulators

Module II
Generation of microwaves by tubes, limitations of conventional tubes, klystron amplifiers - analysis, reflex klystron oscillator-analysis, magnetrons, traveling wave tube (TWT), backward wave oscillator (BWO)-basic principles. Millimetre wave tubes-introduction

Module III
High frequency limitations of transistors, microwave transistors, varators, Manley Rowe relations, parametric amplifiers and frequency multipliers, tunnel diodes, Gunn effect, Gunn Diode oscillators, Avalanche effect, IMPATT & TRAPATT diodes, PIN diodes and their applications, Schottky barrier and backward diodes.

Module IV
Planer transmission lines such as stripline, microstrip line, slotline etc. technology of hybrid MICs, monolithis MICs. Comparison of both MICs. VSWR measurement, microwave power measurement, impedance measurement, frequency measurement, concept of microwave communication-repeaters-frequencies

Text Books
1. Liao S.Y.,”Microwave devices and Circuits”, Prentice Hall Of India, New Delhi, 3rd Ed. 2006

Reference Books
1. Rizzi P.A.,Microwave Engineering,Passive Circuits Hall of India
2. Pozar D.M .,” Microwave Engineering, John Wiley

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class
PTEC09 703: ANALOG & MIXED MOS CIRCUITS

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives
• To impart the concepts of analog & mixed circuit design using MOS transistors

Module I
Active & passive components of basic CMOS technology – parasitics – limitations of CMOS technology
Analog MOS models-small signal, large signal & sub threshold models

Module II

Module III
CMOS operational amplifiers – design of single stage & two stage operational amplifiers – compensation – cascode op amps
Switched capacitor circuits - Switched capacitor amplifiers - Switched capacitor integrators

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70
Module IV
Switched capacitor filters – switched capacitor implementation of ladder filters
Digital phase locked loops - phase detector (XOR & phase frequency detectors) – Charge pump PLL –
non ideal effects - voltage controlled oscillator (current starved & source coupled CMOS
configurations) - loop filter
CMOS comparator design -pre amplification-decision and post amplification stages-transient
response-clocked comparators-analog multiplier- sample and hold circuits

Text Books
1. Allen & Holberg, CMOS Analog Circuit Design, Oxford University Press
2. Razavi B., Design of Analog CMOS integrated Circuits., Tata McGraw Hill

Reference Books
1. Mohammed Ismail& Terri Fiez, Analog VLSI-Signal & Information Processing,MGH
2. Roubik Gregorian& Gabor C Temes, Analog MOS Integrated Circuits for Signal Processing ,
John Wiley & Sons

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz,
literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one
question from each module and not more than two questions
from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There
should be at least one question from each module and not more
than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one
question.

Maximum Total Marks: 70
Objective
After learning this subject students must be able to simulate and implement typical combinational and sequential digital systems in PLDs and express the design in VHDL.

Prerequisite
A good knowledge in digital electronics

Module I

Module II
Introduction to VHDL: Entities and architectures- Behavioral, Data flow and structural descriptions- Identifies, Data objects, Data types and attributes- Delay models- Delta delays-VHDL codes for simple combinational and sequential circuits- State machine Design, simple examples

Module III
Designing with Programmable devices: Programmable Logic Arrays- Programmable Array Logic- sequential- combinational PLDs (Eg: PAL14L4 &PAL12H6), Sequential PLDs (Eg: PAL16R4)- Simple PLDs (Eg: 22V10)- Complex Programmable Logic Devices (Eg: XC9500)- Field Programmable Gate Arrays (Eg: XC 4000 & FLEX 10K)

Module IV
Hazards - Static and Dynamic hazards- Design of hazard free circuits. Elementary ideas of Clock skew, synchronizer failure and metastability

Text Books
2. Perry D.L, VHDL, McGraw Hill

Reference Books
1. John F Wakerly, Digital design principles & practices, Pearson Education.
2. Roth C.H.Jr., Digital system Design using VHDL, PWS Pub.co
3. Kevin Skahill 'VHDL for Programmable Logic' Pearson Education
4. Volnei A Pedroni, Digital electronics and design with VHDL, Elsevier
5. Sudhakar Yalamanchili, Introductory VHDL from simulation to synthesis, Pearson Education.
### Internal Continuous Assessment *(Maximum Marks: 30)*

<table>
<thead>
<tr>
<th>Percentage</th>
<th>Component</th>
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<tbody>
<tr>
<td>60%</td>
<td>Tests (minimum 2)</td>
</tr>
<tr>
<td>30%</td>
<td>Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.</td>
</tr>
<tr>
<td>10%</td>
<td>Regularity in the class</td>
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### University Examination Pattern

**PART A:** *Short answer questions (one/two sentences)*  
5 x 2 marks = 10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** *Analytical/Problem solving questions*  
4 x 5 marks = 20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** *Descriptive/Analytical/Problem solving questions*  
4 x 10 marks = 40 marks  
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 76*
PTEC09 707(P) COMMUNICATION SYSTEMS LAB

Teaching scheme
2 hours practical per week

Credits: 2

Microwave and optical experiments

1. Klystron characteristics o/p power & frequency versus repeller voltage
2. Measurement of frequency and wavelength
3. Slotted line measurements. VSWR (Low & High)
4. Measurement of Impedance
5. Antenna radiation pattern measurements
6. Characteristics of isolator
7. Characteristics of Directional coupler
8. Characteristics of Gunn diode

Optical fibre experiments.
1. To setting up fiber optic analog link.
2. Study of numerical aperture of optical fiber.
3. Study of characteristics of fiber optic LED’s and photo detector.

Hardware
1. Implementation of cyclic code, Hamming code, Gold code generators
2. PN sequence generation
3. Spreader & despreader,
4. Study of Manchester coding and decoding.
5. Study of voice coding and codec chip.

Note: A minimum of 10 experiments must be conducted

Internal Continuous Assessment (Maximum Marks-50)

60% - Laboratory practical and record
30% - Test
10% - Regularity in the class
PART A
1. Comparators using different models
2. Multiplexers & Demultiplexers
3. Shift Registers
4. Ripple adder & Carry look ahead adder
5. Sequence generator & Detector
6. Implementation of a RAM

PART B
7. P and NMOS transistors - \( I_{D}, V_{DS}, V_{T} \) Characteristics – extraction of \( V_{T} \) and body effect factor
8. DC transfer characteristics of an inverter
9. Buffer & Ring Oscillator
10. XOR using different logic styles - comparison
11. Single stage CS amplifiers - their responses for different types of load
12. Single stage source follower - their responses for different types of load
13. Current mirror circuits

Note: (i) A minimum of 10 experiments must be conducted, at least four from each part
(ii) Experiments in part – B should include layout of at least two circuits and their
verification

<table>
<thead>
<tr>
<th>Internal Continuous Assessment (Maximum Marks-50)</th>
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</thead>
<tbody>
<tr>
<td>60% - Laboratory practical and record</td>
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<tr>
<td>30% - Test</td>
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<tr>
<td>10% - Regularity in the class</td>
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<table>
<thead>
<tr>
<th>Semester-End Examination (Maximum Marks-50)</th>
</tr>
</thead>
<tbody>
<tr>
<td>70% - Procedure and tabulation form, Conducting experiment, results and inference</td>
</tr>
<tr>
<td>20% - Viva voce</td>
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<tr>
<td>10% - Fair record</td>
</tr>
</tbody>
</table>
To judge the capacity of the students in converting the theoretical knowledge into practical systems/investigative analysis.

Project work is for duration of two semesters and is expected to be completed in the eighth semester. Each student group consisting of not more than five members is expected to design and develop a complete system or make an investigative analysis of a technical problem in the relevant area. The project may be implemented using software, hardware, or a combination of both. The project work may be undertaken in Electronics/Communication/Computer science or any allied area. Project evaluation committee consisting of the guide and three/four faculty members specialised in Electronics/Communication/Computer science Engg. will perform the screening and evaluation of the projects.

Each project group should submit project synopsis within three weeks from start of seventh semester. Project evaluation committee shall study the feasibility of each project work before giving consent. Literature survey is to be completed in the seventh semester.

Students should execute the project work using the facilities of the institute. However, external projects can be taken up in reputed industries, if that work solves a technical problem of the external firm. Prior sanction should be obtained from the head of department before taking up external project work and there must be an internal guide for such projects.

Each student has to submit an interim report of the project at the end of the 7th semester. Members of the group will present the project details and progress of the project before the committee at the end of the 7th semester.

50% of the marks is to be awarded by the guide and 50% by the evaluation committee.

<table>
<thead>
<tr>
<th>Internal Continuous Assessment</th>
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<tbody>
<tr>
<td>20% - Technical relevance of the project</td>
<td></td>
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<tr>
<td>40% - Literature survey and data collection</td>
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<tr>
<td>20% - Progress of the project and presentation</td>
<td></td>
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<tr>
<td>10% - Report</td>
<td>:</td>
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<tr>
<td>10% - Regularity in the class</td>
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</table>
PTEC09 801: Data and Communication Networks

Teaching scheme
3 hours lecture and 1 hour tutorial per week

Credits: 5

Objectives
- To give the basic ideas of data communication networks- queuing theory, architecture and protocol
- To understand the concept of switching networks

Module I
Queueing Theory: Markov chain-discrete time and continuous time Markov chains- Poisson Process M/M/1 Queue Little’s formula M/M/m/m queueing models-infinite server case State dependnt Queues Birth- Death Process M/G/1 Queue

Module II
Layered Architectures in Data networks: OSI standars architecture and protocols X.25 protocol data link layer-ARQ retransmission strategies Flow control and congestion control in network layer- error control, stop and wait, Sliding windows, Automatic Repeat (ARQ), Asynchronous Protocols, - X MODEM, Y MODEM, Synchronous protocols – Character Oriented and Bit oriented protocols (HDLC).
Routing functions and routing algorithm shortest path routing virtual circuit and datagram networks.TCP/IP protocols

Module III
Local Area Networks IEE 802 standards CSMA/CD, Random access Aloha-pure and slotted aloha Random access using CSMA/CD. Ethernet, Token Bus, Token ring, FDDI, ATM Networks, Distributed Queue Dual Bus, Switched Multimega Bit Data Service. Routing in ATM networks self routing networks Bense Networks addressing and signaling IP over ATM - SONET, SDH- X.25 Protocols, Architecture And Layers of Protocol,

Module IV
Digital switching networks, Two stage Tree stage and N- stage switches, Combination Switches Blocking probability analysis of multistage switches-Lee’s approximation.
Examples of Digital switches-AT & T No.5 ESS switch, DMS-100 switch
Text Books
3. Bertsekas D.& Gallager R.,”Data Networks” Prentice Hall of India
7. Schwartz M.,Telecommunication Networks-Protocols,Modeling and Analysis,Addison Wesley Publishing Company

Reference Books
1 Flood J E., Telecommunication Switching Traffic and Networks, Pearson Education Pvt Ltd.
2 Freeman R L., Telecommunication System Engineering, Wiley Inter Science Publications
3 Das J., Review of Digital Communication, New Age Internal (p) Ltd., Publishers

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class
PTEC09 802: Wireless Mobile Communication

Teaching scheme
1 hour lecture and 1 hour tutorial per week

Credits: 3

Objectives
- To provide a strong background in the basics of wireless mobile communication
- To impart knowledge about the existing GSM and CDMA mobile communication technology

Module I
Cellular concept and frequency reuse, Channel assignment and handoff, cochannel interference-adjacent channel interference –power control for reducing interference –improving capacity in cellular systems-cell splitting –sectoring, Trunking and Erlang capacity calculations.

Module II
Radio wave propagation issues in wireless systems-basic propagation Models- Multipath fading based models, Parameters of mobile multipath channels, Equalization/Rake receiver concepts, Diversity, combining methods and Space-time processing.

Module III
Multiple access techniques; FDMA, TDMA and CDMA. Spread spectrum-cellular CDMA. Principles-Power control- WCDMA-multiuser detection in CDMA.
Module IV
Standards of wireless communication systems – GSM, IMT- 2000, UMTS. GSM architectures, objectives, servicing frequency bands-GSM sub systems, Radio link features in GSM

Text Books

Reference Books
2. Lee W.C.Y. Mobile Cellular Telecommunication’ MGH

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70
PTEC09 805(P): SEMINAR

Teaching scheme 2 hours per week

Credits: 2

OBJECTIVE

To assess the ability of the student to study and present a seminar on a topic of current relevance in electronics/communication/computer science. Or allied areas.

It enables the students to gain knowledge in any of the technically relevant current topics and acquire the confidence in presenting the topic. The student will undertake a detailed study on the chosen topic under the supervision of a faculty member, by referring papers published in reputed journals and conferences. Each student has to submit a seminar report, based on these papers; the report must not be reproduction of any original paper. A committee consisting of three/four faculty members will evaluate the seminar.

Internal Continuous Assessment

20% - Relevance of the topic and literature survey
50% - Presentation and discussion
20% - Report
10% - Regularity in the class and Participation in the seminar

PTEC09 806(P): PROJECT

Teaching scheme 6 hours practical per week

Total Credits: 7
Credits for interim evaluation: 2
Credits for final evaluation: 5

This project work is the continuation of the project initiated in seventh semester. The performance of the students in the project work shall be assessed on a continuous basis by the project evaluation committee through progress seminars and demonstrations conducted during the semester. Each project group should maintain a log book of activities of the project. It should have entries related to the work done, problems faced, solution evolved etc.

There shall be at least an Interim Evaluation and a final evaluation of the project in the 8th semester. Each project group has to submit an interim report in the prescribed format for the interim evaluation.

Each project group should complete the project work in the 8th semester. Each student is expected to prepare a report in the prescribed format, based on the project work. Members of the group will present the relevance, design, implementation, and results of the project before the project evaluation committee comprising of the guide, and three/four faculty members specialised in Electronics/Communication/computer science.
50% of the marks is to be awarded by the guide and 50% by the evaluation committee.

**Internal Continuous Assessment**

<table>
<thead>
<tr>
<th>Percentage</th>
<th>Description</th>
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<tbody>
<tr>
<td>40%</td>
<td>Design and development/Simulation and analysis</td>
</tr>
<tr>
<td>30%</td>
<td>Presentation &amp; demonstration of results</td>
</tr>
<tr>
<td>20%</td>
<td>Report</td>
</tr>
<tr>
<td>10%</td>
<td>Regularity in the class</td>
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PTEC09 807(P): Viva-Voce

**Credits:** 3

**Objective**

- *To examine the knowledge acquired by the student during the B.Tech. course, through an oral examination*

  The students shall prepare for the oral examination based on the theory and laboratory subjects studied in the B.Tech. Course, mini project, seminar, and project. There is only university examination for viva-voce. University will appoint two external examiners and an internal examiner for viva-voce. These examiners shall be senior faculty members having minimum five years teaching experience at engineering degree level. For final viva-voce, candidates should produce certified reports of mini project, seminar, and project (two interim reports and main report). If he/she has undergone industrial training/industrial visit/educational tour or presented a paper in any conference, the certified report/technical paper shall also be brought for the viva-voce.

Allotment of marks for viva-voce shall be as given below.

**Assessment in Viva-voce**

<table>
<thead>
<tr>
<th>Percentage</th>
<th>Description</th>
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</thead>
<tbody>
<tr>
<td>40%</td>
<td>Subjects</td>
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<tr>
<td>30%</td>
<td>Project and Mini Project</td>
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<tr>
<td>20%</td>
<td>Seminar</td>
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<tr>
<td>10%</td>
<td>Industrial training/industrial visit/educational tour or Paper presented at National-level</td>
</tr>
</tbody>
</table>
PTCE09 L25 FINITE ELEMENT METHODS*

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Objective:
To make the background, basic concepts and basic formulation of finite element method clear to the students

Module I


Module II

Module III
Displacement based elements for structural mechanics: formulas for element stiffness matrix and load vector - overview of element stiffness matrices - consistent element nodal vector - equilibrium and compatibility in the solution - convergence requirements - patch test - stress calculation - other formulation methods

Straight sided triangles and tetrahedral: natural coordinates for lines - triangles and tetrahedral - interpolation fields for plane triangles - linear and quadratic triangle - quadratic tetrahedron

Module IV

Coordinate transformation: transformation of vectors - transformation of stress, strain and material properties - transformation of stiffness matrices - transformation of flexibility to stiffness - inclined support - joining dissimilar elements to one another- rigid links - rigid elements

Text books:
1. Bathe K.J., Finite Element Procedures in Engineering Analysis, Prentice Hall of India
2. Cook R.D., Malkus D.S. & Plesha M.F., Concepts & Applications of Finite Element Analysis, John Wiley
Reference books:
1. Desai C.S., Elementary Finite Element Method, Prentice Hall of India

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2) 
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc. 
10% - Regularity in the class

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 76
PTEC09 L 06 SOFT COMPUTING

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Module I

MODULE II

Module III

Module IV

Objectives
- To acquaint the students with the important soft computing methodologies- neural networks, fuzzy logic, genetic algorithms and genetic programming
**Text Books**


**Reference Books**

1. Fakhreddine O Karray, Clarence De Silva, Intelligent Systems Design, Theory, Tools and Application, Pearson Education
6. John Yen, Reza Lengari, *Fuzzy Logic - Intelligence, Control and Information*, Pearson Education

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**Internal Continuous Assessment (Maximum Marks=30)**

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

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**University Examination Pattern**

**PART A:** Short answer questions (one/two sentences)  
5 x 2 marks=10 marks  
All questions are compulsory. There should be at least one question from each Module and not more than two questions from any Module.

**PART B:** Analytical/Problem solving questions  
4 x 5 marks=20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each Module and not more than two questions from any Module.

**PART C:** Descriptive/Analytical/Problem solving questions  
4 x 10 marks=40 marks  
Two questions from each Module with choice to answer one question.

*Maximum Total Marks: 70*
PTEC09  L09: Multimedia Communication Systems

Teaching scheme

2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives

• To impart knowledge in audio/video standards and different types of multimedia networks and technology.

Module I (15 hours)


Module II (14 hours)


Module III (11 hours)

Speech coding standards-Audio coding standards-Still image compression standards-Multimedia conferencing standards. MPEG-1 and –2 compression

MPEG –4 and –7

Module IV (14 hours)


Text Books


Reference Books

2. Ralf Steinmetz, Multimedia Fundamentals, Pearson Education
PTEC09 L012: Antenna Theory & Design

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives
- To impart the concepts different types of antennas and antenna arrays-analysis & synthesis
- To develop understanding about design and modeling of antennas using computational methods

Pre-requisites: EC09 603 Radiation & Propagation

Module I (14 hours)

Module II (14 hours)
Types of Antennas: Traveling wave antennas, Helical antennas, Biconical antennas, sleeve antennas, and Principles of frequency independent Antennas, spiral antennas, and Log -Periodic Antennas. Aperture Antennas- Techniques for evaluating Gain, reflector antennas - Parabolic reflector antenna principles, Axi-symmetric parabolic reflector antenna, offset parabolic reflectors, dual reflector antennas, Gain calculations for reflector antennas, feed antennas for reflectors, field representations, matching the feed to the reflector, general feed model, feed antennas used in practice. Microstrip Antennas-Introduction, rectangular patch, circular patch, bandwidth, coupling, circular polarization, arrays and feed network

Module III (13 hours)

Module IV (13 hours)
CEM for Antennas : Introduction to computational electromagnetics, Introduction to method of moments-Pocklington’s integral equation, source modeling, weighted residuals.Introduction to Finite Difference Time Domain Method-Finite difference and Yee’s algorithm, cell size, numerical stability and dispersion. Absorbing boundary conditions.Introduction to geometrical optics
Text Books

Reference Books
3. I.J.Bhal and P.Bhartia,Micro-strip antennas,Artech house,1980

Internal Continuous Assessment *(Maximum Marks-30)*

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

One of the assignments may be MATLAB / C Implementation to obtain radiation pattern of any antenna

University Examination Pattern

**PART A:** *Short answer questions (one/two sentences)*

5 x 2 marks=10 marks

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** *Analytical/Problem solving questions*

4 x 5 marks=20 marks

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** *Descriptive/Analytical/Problem solving questions*

4 x 10 marks=40 marks

Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTEC09 L016: Embedded Systems

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives
- To give ideas about embedded systems and system development
- To impart knowledge about real time operating systems and microcontrollers

Pre-requisite
EC09 505 Microprocessors and Microcontrollers

Module I

Module II
Real time operating systems: Task and Task States, tasks and data, Message queues - Timer Function - Events - Memory Management, Interrupt Routines in an RTOS environment, basic design Using RTOS. OS services. I/O subsystems. Network operating system. Real time embedded system OS. OS security - Real-Time Embedded Software Development

Module III

Module IV
Embedded system development: Interfacing of external Memory. Interfacing Analog and digital blocks, interfacing of different peripheral devices such as LED, LCD, Graphical LCD, Switches, Relay, stepper motors, ADC, DAC and various sensors. Introduction to assembler, compiler, cross compilers and Integrated Development Environment (IDE).
Text Books

4. K.J. Ayala ,The 8051 Microcontroller , Penram International
5. J B Peatman, Design with PIC Microcontrollers, Prentice Hall
6. Dhananjay Gadre ,Programming and Customizing the AVR Microcontroller, MGH

Reference Books

6. Microchip Manual for PIC 18F 452

Internal Continuous Assessment (Maximum Marks-30)

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 76
Objectives

- A very basic expose to students on sub-micron device technology & to discuss the alternate options in devices

Module I

- Sub micron MOSFET – effects - junction depth – oxide thickness – depletion widths – isolation – MOSFET device design – scaling – non scaling effects – channel engineering
- SOI MOSFET - Strained silicon – realisation – features – Low temperature CMOS

Module II

- MESFET – Basic structure – DC characteristics – basic design – small signal operation – Large signal operation – digital operation
- Hetero structures – Silicon based HBT – GaAlAs/GaAs HBT - modulation doped structures
- Free & confined electrons -1D & 3D space – partially confined electrons – Quantum dots wires and wells - logic realisation using QD – conductivity in metallic nanowires

Module III

- Tunnelling effect – tunnelling diode - Resonant tunnelling devices – Digital circuits using RTD – memories – basic gates
- Coulomb blockade - Single Electron Transistor - Circuit design

Module IV

- Production of nanolayers – PVD –CVD- Epitaxy – Ion implantation – formation of SiO2 layer – characterisation – applications
- Fabrication of nanostructures–lithography–nano imprint lithography –split gate technology– self assembly

Reference Books

2. George W Hanson, Fundamentals of nanoelectronics, Pearson Education
3. Sze S. M, High Speed Semiconductor Devices, Wiley interscience
4. Fahrner, Nanotechnology & Nanoelectronics, Springer
5. Goser, Glosekotter, Dienstuhl, Nanoelectronics & Nanosystems, Springer
**Internal Continuous Assessment** *(Maximum Marks-30)*

- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity in the class

**Note:** One of the assignments shall be a literary survey on any topic in this area.

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**University Examination Pattern**

**PART A:** *Short answer questions (one/two sentences)*

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** *Analytical/Problem solving questions*

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** *Descriptive/Analytical/Problem solving questions*

Two questions from each module with choice to answer one question.

*Maximum Total Marks: 76*
PTEC09  L022: Advanced Digital Signal Processing

Objectives
- To give ideas of multirate systems and filter banks
- To impart knowledge about wavelet transforms & their applications

Pre-requisite
EC09 501 Digital Signal Processing

Module I
Multirate system fundamentals: Basic multirate operations, up-sampling and down sampling: Time domain and frequency domain analysis, Identities of multirate operations, Interpolator and decimator design, Rate conversion, Polyphase representation.

Module II
Multirate Filter banks: Maximally decimated filter banks, Quadrature mirror filter (QMF) banks, Polyphase representation, Errors in the QMF banks: Aliasing and Imaging Method of cancelling aliasing error, Amplitude and phase distortion, Prefect reconstruction (PR) QMF banks, PR condition, M-channel perfect reconstruction filter banks, Paraunitary PR Filter Banks

Module III
Wavelets: Fundamentals of signal decomposition - brief overview of Fourier transform and short time Fourier transform - time frequency resolution - Continuous wavelet transform - different wavelets- DWT - wavelet decomposition - approximation of vectors in nested linear vector spaces - example of MRA - orthogonal wavelet decomposition based on the Haar wavelet - digital filter implementation of the Haar wavelet decomposition

Module IV
Wavelet applications: Image compression - EZW algorithm - Audio compression - signal denoising techniques- different types- edge detection. Lossless compression
Text Books
1. P. P. Vaidyanathan, Multirate Systems and Filter Banks, Pearson Education, Delhi, 2004
2. K. P. Soman and K. I. Ramachandran, Insight into Wavelets, Prentice Hall of India, New Delhi, 2004

Reference Books

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70
PTEC09 L025: Biomedical Instrumentation

Teaching scheme

Credit: 4
2 hours lecture and 1 hour tutorial per week

Objectives

- To impart knowledge about the principle and working of different types of biomedical electronic equipments/devices

Module I

Electrical activity of excitable cells - SD curve-functional organization of the peripheral nervous system-electrocardiogram (in detail with all lead systems)-electroencephalogram-electromyogram –electroneurogram- electrode –electrolyte interface-polarisation-polarisable and non polarisable electrodes- surface electrodes –needle electrodes-micro electrodes-practical hints for using electrodes-'skin- electrodes’ equivalent circuit-characteristics of ‘bio-amplifiers’

Module II


Module III


Module IV

Physiological effects of electricity-important susceptibility parameters-macro shock hazards-micro shock hazards-protection against shock-electrical isolation- electrical safety analyzers-measurements of pH,pCO2, and PO2

Text Books

1. Webster J,’ Medical Instrumentation-Application and Design’, John Wiley
2. Handbook of Biomedical Instrumentation, Tata-Migraw Hill, New Delhi

Reference Books

2. Encyclopedia of Medical Devices and Instrumentation Wiley
## Internal Continuous Assessment *(Maximum Marks: 30)*

<table>
<thead>
<tr>
<th>Percentage</th>
<th>Component</th>
<th>Details</th>
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<tbody>
<tr>
<td>60%</td>
<td>Tests</td>
<td>(minimum 2)</td>
</tr>
<tr>
<td>30%</td>
<td>Assignments</td>
<td>(minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.</td>
</tr>
<tr>
<td>10%</td>
<td>Regularity in the class</td>
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## University Examination Pattern

**PART A:** Short answer questions *(one/two sentences)*

5 x 2 marks = 10 marks

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions

4 x 5 marks = 20 marks

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions

4 x 10 marks = 40 marks

Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTME09 L23: Industrial Safety Engineering

Teaching scheme

2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives

- To provide on concept of safety in industry, principle of accident prevention, major hazards, consequences and concept of reliability.

Pre-requisites: Nil

Module I

Introduction to the concept of safety-Need-safety provisions in the factory Act-Laws related to the industrial safety-Measurement of safety performance, Safety Audit, Work permit system, injury and accidents-Definitions-Unsafe act –unsafe condition- causes, investigations and prevention of accidents, hazards, type of industrial hazards-nature, causes and control measures, hazard identifications and control techniques-HAZOP, FMEA,FMECA etc.

Module II


Module III

Logics of consequence analysis-Estimation-Toxic release and toxic effects-Threshold limit values, Emergency planning and preparedness, Air pollution-classification- Dispersion modeling -pollution source and effects- -control method and equipments-Gravitational settling chambers-cyclone separators-Fabric filter systems-scrubbers etc.

Module IV

Concept of reliability-Definition-Failure rate and Hazard function, System reliability models-series, parallel systems, reliability hazard function for distribution functions-exponential-normal –lognormal-weibull and gamma distribution.

Text books

3. C.S.Rao, Environmental Pollution Control Engineering, New Age International Limited
Reference books


Internal Continuous Assessment (Maximum Marks-30)

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

**PART A:** Short answer questions (one/two sentences)  
5 x 2 marks = 10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions  
4 x 5 marks = 20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions  
4 x 10 marks = 40 marks
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTCS09 L23: SIMULATION AND MODELING

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives

- To teach the students how to reproduce real-world events or process under controlled laboratory conditions, using mainly mathematical models.

Module I
Introduction - systems and models - computer simulation and its applications - continuous system simulation - modeling continuous systems - simulation of continuous systems - discrete system simulation - methodology - event scheduling and process interaction approaches - random number generation - testing of randomness - generation of stochastic variates - random samples from continuous distributions - uniform distribution - exponential distribution - Erlang distribution - gamma distribution - normal distribution - beta distribution - random samples from discrete distributions - Bernoulli - discrete uniform - binomial - geometric and poisson

Module II
Evaluation of simulation experiments - verification and validation of simulation experiments - statistical reliability in evaluating simulation experiments - confidence intervals for terminating simulation runs - simulation languages - programming considerations - general features of GPSS - SIM SCRIPT and SIMULA.

Module III

Module IV

Reference Books
1. C. Deo N., System Simulation And Digital Computer, Prentice Hall of India.
**Internal Continuous Assessment (Maximum Marks-30)**

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

**Note:** One of the assignments shall be computer-based simulation. One of the tests shall be computer-based (practical).

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**University Examination Pattern**

**PART A:** *Short answer questions (one/two sentences)*

5 x 2 marks = 10 marks

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** *Analytical/Problem solving questions*

4 x 5 marks = 20 marks

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** *Descriptive/Analytical/Problem solving questions*

4 x 10 marks = 40 marks

Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
EC09  L07: High Speed Digital Design

Teaching scheme
3 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives
- To give the basic ideas involved in high speed digital design
- To understand the transmission line effects and cross talk and the effects of terminations & vias

Module I
Introduction to high-speed digital design - frequency, time and distance - capacitance and inductance effects - high speed properties of logic gates - speed and power - measurement techniques - rise time and bandwidth of oscilloscope probes - self inductance, signal pickup and loading effects of probes - observing crosstalk

Module II
Transmission line effects and crosstalk - transmission lines - point to point wiring - infinite uniform transmission lines - effects of source and load impedance - special transmission line cases - line impedance and propagation delay - ground planes and layer stacking - crosstalk in solid ground planes, slotted ground planes and cross-hatched ground planes - near and far end crosstalk

Module III
Terminations and vias - terminations - end, source and middle terminations - AC biasing for end terminations - resistor selection - crosstalk in terminators - properties of vias - mechanical properties of vias - capacitance of vias - inductance of vias - return current and its relation to vias

Module IV
Stable reference voltage and clock distribution - stable voltage reference - distribution of uniform voltage - choosing a bypass capacitor - clock distribution - clock skew and methods to reduce skew - controlling crosstalk on clock lines - delay adjustments - clock oscillators and clock jitter

Text Books

Internal Continuous Assessment (Maximum Marks-30)
- 60% - Tests (minimum 2)
- 30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- 10% - Regularity in the class

2. Note: One of the assignments shall be Simulation of any active device characteristics using any high frequency tool
University Examination Pattern

**PART A:** Short answer questions (one/two sentences)  
5 x 2 marks = 10 marks

All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions  
4 x 5 marks = 20 marks

Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions  
4 x 10 marks = 40 marks

Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*

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**PTEC09 L010: Management Information System**

**Teaching scheme**
2 hours lecture and 1 hour tutorial per week

**Credits:** 4

**Objectives**
To create general awareness and exposure of management principles relevant to industrial sector

**Module I**
Information systems-Functions of management-Level of management –Frame work for information systems-Systems approach-Systems concept- Systems and their environment –Effects of system approach in information system design-Using systems approach in problem solving –Strategic uses of information

**Module II**
An overview of computer hardware and software components –File and database management systems-Introduction to network components-Topologies and types-remote access- The reason for managers to implement networks-Distributed systems- The internet and office communications.
Module III  
Application of information systems to functional-Tactical and strategic areas if management, decision support systems and expert systems.

Module IV  
Information systems planning-Critical success factor-Business system planning-Ends/means analysis-Organizing the information system plan-System analysis and design and design-Alternative application development approaches-organization of data processing- Security and ethical issues of information systems.

Text Books  

Reference Books  
1. London K.C & Landon P.J- Management Information Systems:  
5. Effy Oz-Management Information Systems, Vikas Publishing

Internal Continuous Assessment (Maximum Marks-30)  
60% - Tests (minimum 2)  
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.  
10% - Regularity in the class

University Examination Pattern  
PART A: Short answer questions (one/two sentences)  
5 x 2 marks=10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions  
4 x 5 marks=20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions  
4 x 10 marks=40 marks  
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70
EC09 L013: Microwave Active Devices & Circuits

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives
- To develop understanding about design & analysis of various microwave active circuits
- To impart knowledge about MICs and MMICs

Pre-requisites:
EC09 EC09 702 Microwave Engineering

Module I
Active Microwave Circuits-Noise in microwave circuits-source of noise, noise power and equivalent noise temperature, noise figure; detectors and mixers-diode rectifiers and detectors, single ended mixer, balanced mixer, types of mixers; PIN diode and control circuits-single pole switchwes, phase shifters

Module II
Microwave amplifiers and oscillators-Characteristics of transistors-FETs, bipolar transistors; gain and stability. Single stage amplifier design-design for maximum gain and low noise amplifiers, broadband amplifier design, oscillator design

Module III
Microwave filters-Periodic structures-analysis, $k$-$\beta$ diagram and wave velocities; filter design by image parameter method-image impedance and transfer functions for two port networks, constant $k$-filter sections, $m$-derived filter sections, composite filters, filter transformations-impedance and frequency scaling, bandpass and bandstop transformations; coupled line filters, filter uysing coupled resonators

Module IV
Microwave Integrated circuits-hybrid MICs, Monolithic MICs, MIC materials-substate, conductor, dielectric materials, types of MICs, hybrid versus monolithic MICs

Text Books
1. Davis M Pozar, ‘Microwave Engineering’ 2nd Ed. Wiley India, 2008

Reference Books
1. O.P.Gandhi, Microwave design engineering and applications, Elsevier Science,1991
### Internal Continuous Assessment (Maximum Marks-30)

- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity in the class

**Note:** One of the assignments shall be Simulation of any active device characteristics using any high frequency tool

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### University Examination Pattern

<table>
<thead>
<tr>
<th>PART A:</th>
<th>Short answer questions (one/two sentences)</th>
<th>5 x 2 marks=10 marks</th>
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<tbody>
<tr>
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<td>All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.</td>
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</table>

<table>
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<tr>
<th>PART B:</th>
<th>Analytical/Problem solving questions</th>
<th>4 x 5 marks=20 marks</th>
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<td>Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.</td>
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<table>
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<tr>
<th>PART C:</th>
<th>Descriptive/Analytical/Problem solving questions</th>
<th>4 x 10 marks=40 marks</th>
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</thead>
<tbody>
<tr>
<td></td>
<td>Two questions from each module with choice to answer one question.</td>
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</tr>
</tbody>
</table>

**Maximum Total Marks: 70**
PTEC09  L017: Photonic Switching And Network

Teaching scheme
3 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives
- To give ideas about photonic switching and associated circuits
- To impart knowledge about design and analysis of common optical systems and networks

Module I
Introduction: Overview of the architectures and principles of optical systems and networks; Access networks; LANS, WANS & MANS; SONET, SDH and ATM.
Components for Optical Networks: Fused fibre devices such as couplers, WDMs and WFCs; filters and WDMs such as interference filters, Fabry Perot etalons and Bragg gratings; optical isolators; integrated optic modulators and switches; wavelength converters, Dispersion Compensating techniques.

Module II
Optical Amplifiers (EDFAs and SOAs): Principles of operation; gain characteristics; wavelength characteristics, cross talk and wavelength conversion; noise characteristics and noise figure; characteristics of amplifiers cascades.

Module III
Design and Analysis of Optically Amplified links: systems performance analysis and power budget analysis for BERs of 10^{-9} for optically Amplified links.

Module IV
Design and Analysis of Common Optical Systems and Networks: Power budgets, issues of component specification and tolerances; PONs, BPONs, WDM systems, wavelength routing networks and all optically switched systems. Optical Fiber impairment issues like: higher order dispersion, fiber nonlinearities in optical systems and Networks, optical solitons.

Text Books
2. OptSim/OptiSystem Manuals.
3. Abdellatif Marrakchi, “Photonic Switching and Interconnects,” Marcel Dekker, November 1993
**Internal Continuous Assessment (Maximum Marks-30)**

- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity in the class

One of the assignments shall be Design and analysis using simulation tool OptSim or OptiSystem and Artifex.

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**University Examination Pattern**

**PART A:** Short answer questions (one/two sentences)  
5 x 2 marks = 10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions  
4 x 5 marks = 20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions  
4 x 10 marks = 40 marks  
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 76*
PTEC09 L20: Mobile Computing

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives

- To give basic concepts of mobile computing
- To impart knowledge about various wireless systems, LANs, and Mobile IP

Module I

Module II
WLANs (Wireless LANs)- Wi-Fi- IEEE 802.11- architecture-services- IEEE 802.11a & 802.11b standard-HIPERLAN-, Bluetooth -IEEE 802.15-WiMAX-IEEE 802.16

Module III

Module IV
Wireless Application Protocol (WAP): The Mobile Internet standard-architecture-components of WAP standard WAP Gateway and Protocols-WAP2.0- wireless mark up Languages (WML)-basics

Text Books


Reference Books

2. A F Molisch, ’Wireless communications’, Wiley India, 2005
4. Steele, ’GSM, CDMA One & 3G systems’ Wiley India, New Delhi, 2008
**Internal Continuous Assessment (Maximum Marks-30)**

- 60% - Tests (minimum 2)
- 30% - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- 10% - Regularity in the class

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**University Examination Pattern**

**PART A:** Short answer questions (one/two sentences)  
5 x 2 marks = 10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** Analytical/Problem solving questions  
4 x 5 marks = 20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** Descriptive/Analytical/Problem solving questions  
4 x 10 marks = 40 marks  
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTEC09 L023: Data Structures & Algorithms

**Teaching scheme**
2 hours lecture and 1 hour tutorial per week

**Credits:** 4

**Objectives**
- To give ideas of basic data structures
- To impart knowledge about algorithm specification

**Module I**

**Module II**
Linked Lists - Linked stacks and queues - Doubly linked lists - Polynomial representation using linked lists, Strings – Data representation – Pattern matching

**Module III**

**Module IV**

**Text Books**
1. Classic Data Structures: Samanta, PHI
2. Data Structures and program design in C: Robert Kruse, Pearson Education Asia
3. An introduction to Data Structures with applications: Trembley & Sorenson, McGraw Hill

**Reference Books**
2. Data Structures using C & C++: Langsam, Augenstein & Tanenbaum
4. Algorithms + Data Structures & Programs: N.Wirth, PHI
5. Data structures in Java: Thomas Standish, Pearson Education Asia
Internal Continuous Assessment *(Maximum Marks-30)*

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

*PART A:* Short answer questions (one/two sentences)  
5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

*PART B:* Analytical/Problem solving questions  
4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

*PART C:* Descriptive/Analytical/Problem solving questions  
4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*

PTEE09 L24 MECHATRONICS

Teaching scheme  
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives:  
- To provide knowledge on the fundamentals of mechatronics, Numerical control machine tools, part programming and robotics.

Module I
Fundamentals of numerical control-advantages of NC systems- Classification of NC systems- Point to point and contouring systems- NC and CNC – Incremental and absolute systems-Open loop and closed loop systems-features of NC machine tools- Fundamentals of machining-Design consideration of NC machine tools-Methods of improving machine accuracy and productivity- Special tool holders
Module II

Module III

Module IV

References

Internal Continuous Assessment (Maximum Marks=30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each Module and not more than two questions from any Module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each Module and not more than two questions from any Module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each Module with choice to answer one question.

Maximum Total Marks: 70
PTEC09 L015: Television & Radar Engineering

Objectives

• To give the basic ideas & operating principles of different types of b/w as well as color CTV and radar (both transmitter and receiver) and their uses.
• To create the awareness about the different standards of TV systems used in different countries and their basic principles.

Module I

Principles of TV- image continuity- Horizontal and vertical scanning- number of scanning lines- flicker- interlaced scanning fine structure – Composite video signal- VSB transmission and reception- Channel bandwidth - positive and negative modulation- Transmitter – receiver – monochrome picture tube- CCD camera

Module II

Colour TV- compatibility- Three colour theory- Grassnans laws- luminance, hue and saturation - Colour TV Camera tube- Picture tube- Pincushion correction techniques- auto degaussing circuits- frequency interleaving- Bandwidth for color signal transmission- modulation of colour difference signals- colour burst- weighting factors- principles of NTSC,PAL and SECAM coder and decoder- Block Diagram of Digital T.V-Transmitter- receiver- HDTV, Concept of Plasma Screen

Module III

Radar system- Simple form of radar equation- Radar block diagram- radar frequencies- Prediction of range performance- minimum detectable signal- receiver noise- pulse reception- frequency and range ambiguities- antenna parameter – Doppler effect= system losses and propagation effects.

Module IV


Teaching scheme

Credits: 4
2 hours lecture and 1 hour tutorial per week

Text Books

3. Bernard Grob& Charles E. Herndon,Basic Television and Video Systems,

Reference Books

2. Damacher P. Digital Broadcasting ,IEE Telecommunication Series

Internal Continuous Assessment (Maximum Marks-30)

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class
PTCS09 L25: PATTERN RECOGNITION

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives

- To impart a basic knowledge on pattern recognition and to give a sound idea on the topics of parameter estimation and supervised learning, linear discriminant functions and syntactic approach to PR.
- To provide a strong foundation to students to understand and design pattern recognition systems.

Module I
Introduction - introduction to statistical - syntactic and descriptive approaches - features and feature extraction - learning - Bayes Decision theory - introduction - continuous case - 2-category classification - minimum error rate classification - classifiers - discriminant functions - and decision surfaces - error probabilities and integrals - normal density - discriminant functions for normal density

Module II
Parameter estimation and supervised learning - maximum likelihood estimation - the Bayes classifier - learning the mean of a normal density - general bayesian learning - nonparametric technic - density estimation - parzen windows - k-nearest neighbour estimation - estimation of posterior probabilities - nearest-neighbour rule - k-nearest neighbour rule

University Examination Pattern

**PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks**
All questions are compulsory. There should be at least one question from each Module and not more than two questions from any Module.

**PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks**
Candidates have to answer four questions out of six. There should be at least one question from each Module and not more than two questions from any Module.

**PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks**
Two questions from each Module with choice to answer one question.

Maximum Total Marks: 70
Module III
Linear discriminant functions - linear discriminant functions and decision surfaces - generalised linear discriminant functions - 2-category linearly separable case - non-separable behaviour - linear programming procedures - clustering - data description and clustering - similarity measures - criterion functions for clustering

Module IV
Syntactic approach to PR - introduction to pattern grammars and languages - higher dimensional grammars - tree, graph, web, plex, and shape grammars - stochastic grammars - attribute grammars - parsing techniques - grammatical inference

Text Books
1. Duda & Hart P.E, Pattern Classification And Scene Analysis, John Wiley

Reference Books
1. Fu K.S., Syntactic Pattern Recognition And Applications, Prentice Hall, Eaglewood cliffs

Internal Continuous Assessment (Maximum Marks-30)
60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
10% - Regularity in the class

University Examination Pattern

PART A: Short answer questions (one/two sentences) 5 x 2 marks=10 marks
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

PART B: Analytical/Problem solving questions 4 x 5 marks=20 marks
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

PART C: Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

Maximum Total Marks: 70
Objective

*To introduce the following concepts to the students*

- manufacturing of a micro device from material selection to final product design
- the various materials used in microfabrication and their applications
- how basic engineering design can couple with practice manufacturing techniques for getting a MEMS device
- the changes in properties when the dimensions of the system are scaled

Module I


Module II


Module III

submicrometer scale - Thermal conductivity of thin films - heat conduction equation for thin films.


Module IV


Text Book

Reference Books

University Examination Pattern

**PART A:** Short answer questions (one/two sentences) 5 x 2 marks=10 marks
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**PART C:** Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTEC09 L011: Cryptography And Network Security

Objectives

- To impart the basic concepts of network security
- To develop understanding about various cryptography schemes and securing networks.

Pre-requisites: EC09 801 Data Communication & Network

Module I
Overview: Services, Mechanisms and attacks, OSI security architecture, Model for network security.
Block Ciphers and DES (Data Encryption Standards): Simplified DES, Block cipher principles, DES, Strength of DES, Block cipher design principles, Block cipher modes of operation, Problems.

Module II
Public Key Cryptography and RSA: Principles of public key cryptosystems, RSA algorithm, Problems.
Other Public Key Crypto Systems and Key Management: Key management, Diffie-Hellman key exchange, Elliptic curve arithmetic, Elliptic curve cryptography, Problems.

Module III

Module IV
Electronic Mail Security: Pretty good privacy, S/MIME, Data compression using ZIP, Radix-64 conversion, PGP random number generator.
Firewalls: Firewall design principles; Trusted systems, Problems.

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Text Books

Reference Books
**EC09 L014: Internet Technology**

**Objectives**
- To make the student aware of the various protocols used in internet.

**Module I**
- Computer networks and the internet-principles of application-layer protocols-HTTP- FTP-e-mail-DNS-socket programming with TCP/UDP-web servers-web pages design using HTML and XML

**Module II**
- Multimedia networking–applications – streaming stored audio and video-internet telephony-RTP-scheduling and policing mechanisms-integrated services- RSVP-differentiated services-network management-the internet network management framework

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**Internal Continuous Assessment (Maximum Marks-30)**
- 60% - Tests (minimum 2)
- 30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- 10% - Regularity in the class

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**University Examination Pattern**

**PART A:** Short answer questions (one/two sentences) 5 x 2 marks=10 marks
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**PART C:** Descriptive/Analytical/Problem solving questions 4 x 10 marks=40 marks
- Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*

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**Teaching scheme**
- credits: 4
- 2 hours lecture and 1 hour tutorial per week

**Credits:** 4
Module III
Network security – E-mail security-privacy-S/MIME-IP security-overview-architecture-authentication-header and payload-combining security associations-key management- web security-SSL and transport layer security – SET-systems security-intruders and viruses-firewalls-design-trusted systems.

Module IV

Text Books
2. Stallings W., Cryptography and Network Security Principles and practice., Pearson Education Asia, Module III
3. Schiller J., Mobile Communications, Addison Wesley, Module IV

Reference Books
2. Greenlaw R& Hepp E, In-line/On-line; Fundamentals Of the Internet And the World Wide Web, Tata Mc Graw Hill
Objectives

• To provide basic knowledge about nano/microdevices, mathematical modeling of electromechanical systems and applications

Module I
Biological analogies of Nano and Micro-electromechanical systems (NMEMS)-Fabrication of MEMS-assembling and packing—applications of NMEMS

Module II
Mathematical models and design of NMEMS- NMEMS architecture-electro magnetics and its applications is NMEMS–Molecular and Nano structure dynamics-molecular wires and molecular circuits-thermo analysis and heat equation.

Module III
Carbon nanotubes and nono devices-structural design of nano and MEM actuators and sensors-configurations and structural design of motion nano and micro-structures.

Module IV
Algebra of sets-direct current micro machines-mathematical models of induction motors-micro synchronous machines-single phase reluctance motors-stepper motors-synchronous reference frames-control of NMEMS

Text Books


Internal Continuous Assessment (Maximum Marks-30)

60% - Tests (minimum 2)
30% - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
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One of the assignments shall be simulation using any of the tools
Objectives

• To give ideas & techniques of image & video processing
• To impart knowledge about image filtering, restoration & reconstruction

Pre-requisite

EC09 501 Digital Signal Processing

Module I

Module II

Module III
Image Analysis & Computer Vision: Spatial feature extraction, Transform features, Edge detection, Boundary Extraction, Boundary representation, Region representation, Moment representation, Structure, Shape features, Texture, Scene matching & detection, Image segmentation, Classification Techniques.

Module IV
Video Processing: Fundamental Concepts in Video – Types of video signals, Analog video, Digital video, Color models in video, Video Compression Techniques – Motion compensation, Search for motion vectors, H.261, H.263, MPEG I, MPEG 2, MPEG 4, MPEG 7 and beyond, Content based video indexing

Text Books

Internal Continuous Assessment (Maximum Marks-30)
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10% - Regularity in the class
PTEC09 L24: ELECTRONIC PACKAGING

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives

- Introduction to packaging technologies, technology drivers, electrical performance, thermal management, materials, optoelectronics, RF integration, reliability, system issues, assembly, and testing.

Module I
Electrical Design - Interconnect Capacitance, Resistance and Inductance fundamentals - Transmission Lines (basic concepts) - Clock Distribution - Noise Sources - power Distribution – signal distribution – EMI - Digital and RF Issues

Module II
Thermal Management - Heat-transfer fundamentals - Thermal conductivity and resistance - Conduction, convection and radiation – Cooling requirements
Reliability - Basic concepts - Environmental interactions - Thermal mismatch and fatigue – failures – thermomechanically induced – electrically induces – chemically induced-

Module III
Single chip packaging – functions, types, materials processes, properties, characteristics, trends
Multi chip packaging – types, design, comparison, trends  
IC assembly – purpose, requirements, technologies – wire bonding, TAB, flip chip  
Wafer level packaging - technologies, reliability, wafer level burn – in and test  

**Module IV**
Passives – discrete, integrated, embedded – encapsulation and sealing – fundamentals, requirements, materials, processes  
PWB – fundamentals, standards, limitations – microvia boards – PWB assembly – SMT- Through hole assembly – design challenges  

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**Internal Continuous Assessment (Maximum Marks-30)**  
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30% - Assignments (minimum 2) such as homework, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.  
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**University Examination Pattern**

**PART A:** *Short answer questions* (one/two sentences)  
5 x 2 marks = 10 marks  
All questions are compulsory. There should be at least one question from each module and not more than two questions from any module.

**PART B:** *Analytical/Problem solving questions*  
4 x 5 marks = 20 marks  
Candidates have to answer four questions out of six. There should be at least one question from each module and not more than two questions from any module.

**PART C:** *Descriptive/Analytical/Problem solving questions*  
4 x 10 marks = 40 marks  
Two questions from each module with choice to answer one question.

*Maximum Total Marks: 70*
PTEE09 L 25 ROBOTICS AND AUTOMATION

Teaching scheme
2 hours lecture and 1 hour tutorial per week

Credits: 4

Objectives
- To give an introduction of industrial robotics and automation

Module I

Module II

Module III

Module IV

Text Books
2. K. S. Fu, R. C. Gonzalez, C. S. G. Lee, Robotics, Control, Sensing and Intelligence, McGraw Hill
**Internal Continuous Assessment (Maximum Marks-30)**

- **60%** - Tests (minimum 2)
- **30%** - Assignments (minimum 2) such as home work, problem solving, group discussions, quiz, literature survey, seminar, term-project, software exercises, etc.
- **10%** - Regularity in the class

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**University Examination Pattern**

**PART A:** *Short answer questions (one/two sentences)*  \( 5 \times 2 \text{ marks} = 10 \text{ marks} 

All questions are compulsory. There should be at least one question from each Module and not more than two questions from any Module.

**PART B:** *Analytical/Problem solving questions*  \( 4 \times 5 \text{ marks} = 20 \text{ marks} 

Candidates have to answer four questions out of six. There should be at least one question from each Module and not more than two questions from any Module.

**PART C:** *Descriptive/Analytical/Problem solving questions*  \( 4 \times 10 \text{ marks} = 40 \text{ marks} 

Two questions from each Module with choice to answer one question.

*Maximum Total Marks: 70*